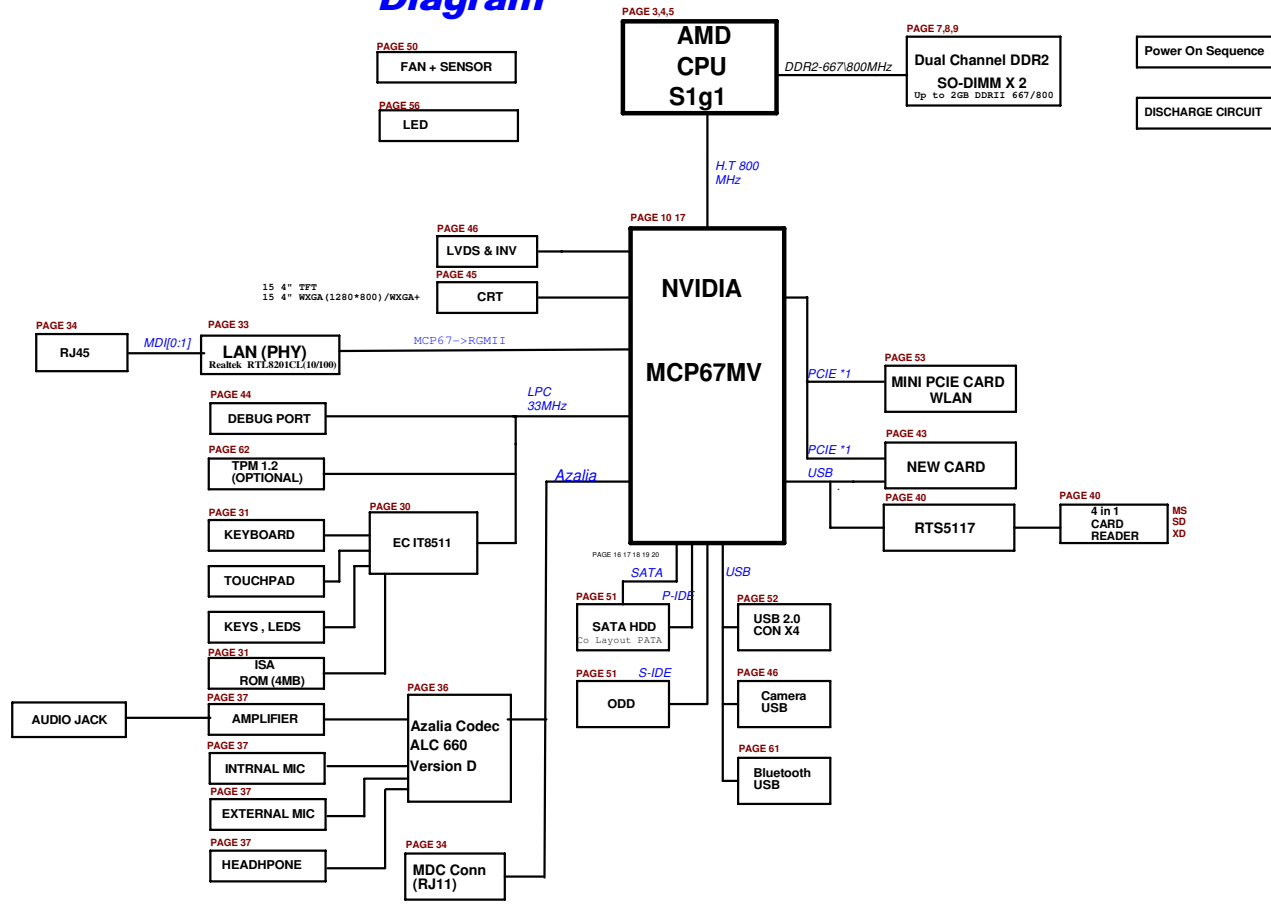


F5N Block Diagram




<http://konweer.kiev.ua>

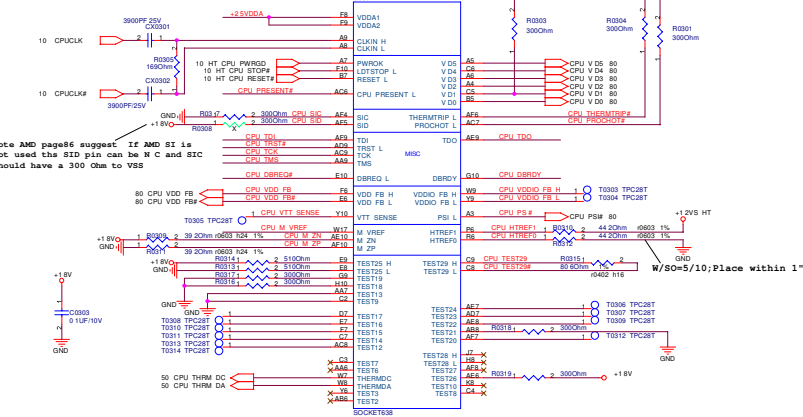
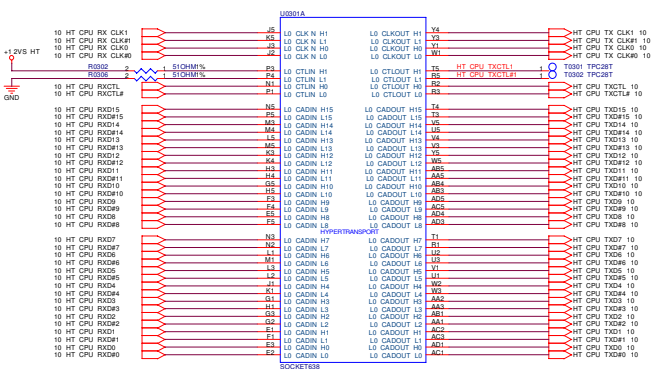
« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Doc No.	Doc Name	Rev	
FSU	FSU	1.0	
Date: 2008-05-29 10:00	Drawn: 1	21	28

<http://konweer.kiev.ua>

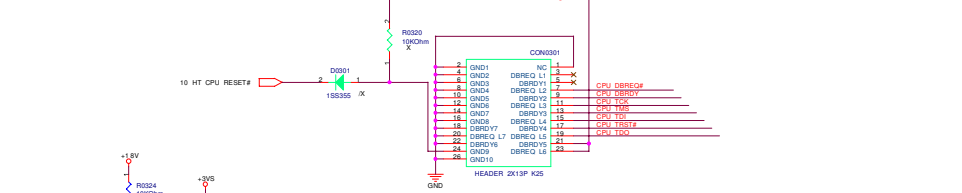
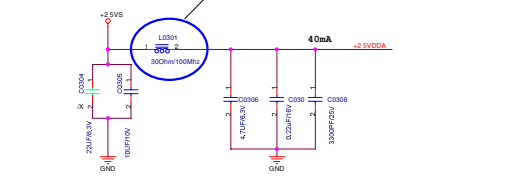
« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 3 of 28	

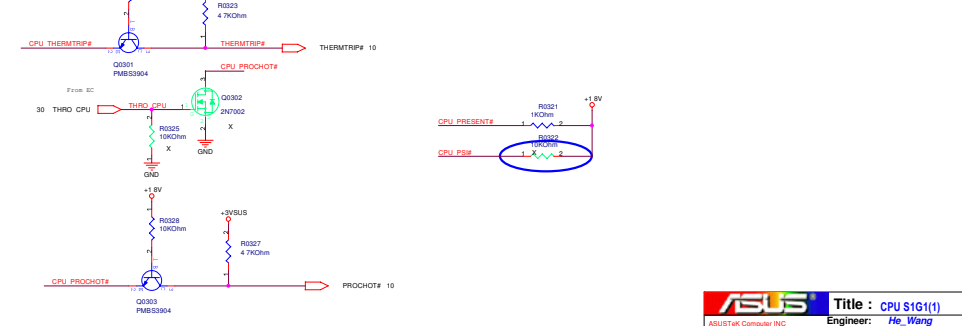
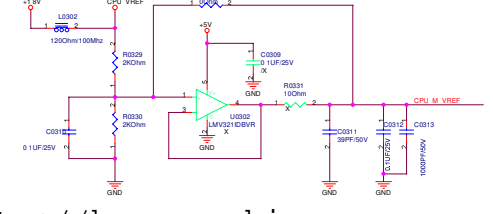


12G011306380

Next and design guide (DC Resistance <= 4 ohm, Impedance <= 35ohm)



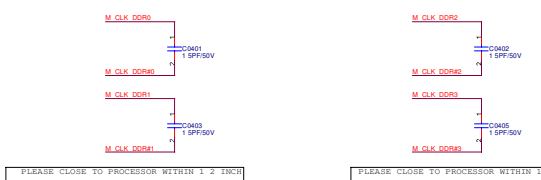
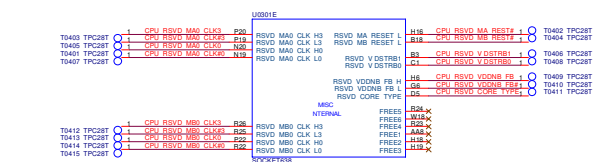
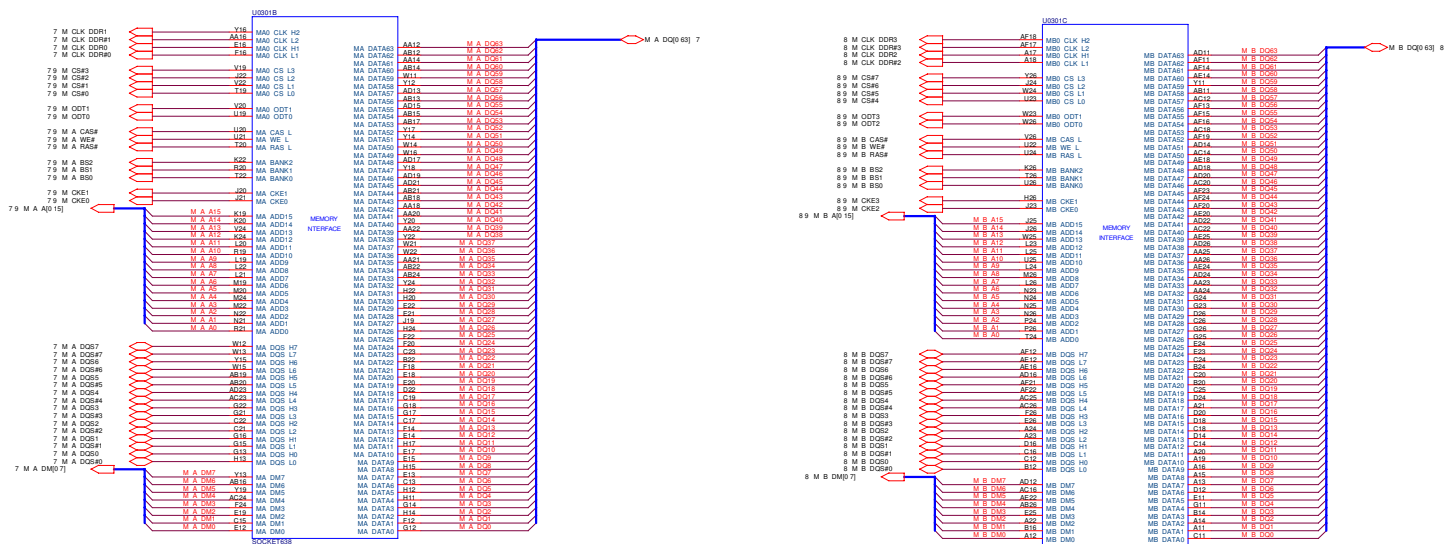
15mil trace, 20mil space / Shorter than 6"



<http://konweer.kiev.ua>

Kennedy_Zhang

ASUS		Title : CPU SIG(1)	
ASUSTeK Computer Inc.		Engineer: He Wang	
FSU	Rev		
Date: 08-14-2007	Sheet: 3		

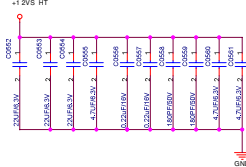
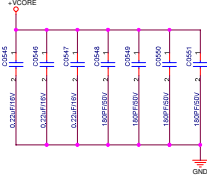
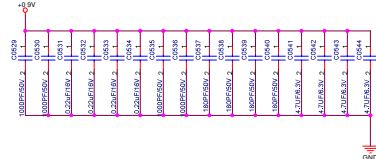
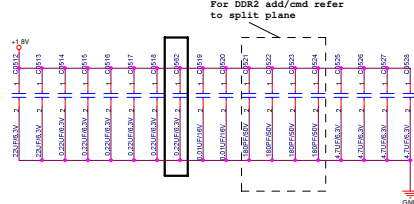
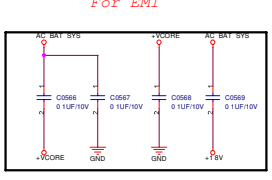
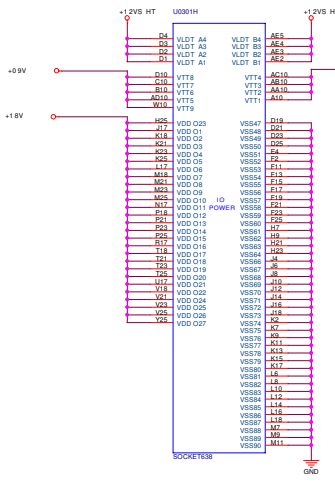
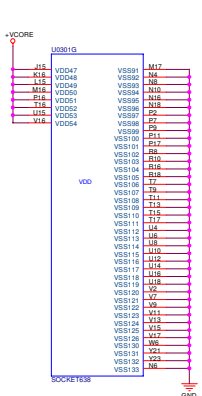
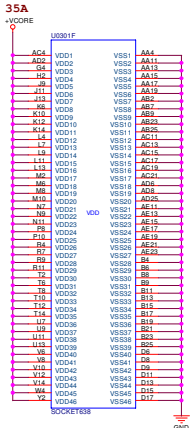


PLEASE CLOSE TO PROCESSOR WITHIN 1.2 INCH

		Title: FSU	
<small>ASUSTeK Computer Inc.</small>		<small>Engineer: He Wang</small>	
<small>Rev</small>	<small>PCB Name</small>	<small>Rev</small>	<small>Rev</small>
<small>1</small>	<small>FSU</small>	<small>1</small>	<small>1.0</small>
<small>Date</small>	<small>Rev</small>	<small>Sheet</small>	<small>of</small>
<small>2011-10-20</small>	<small>1</small>	<small>4</small>	<small>4</small>

<http://konweer.kiev.ua>

« Kennedy_Zhang »




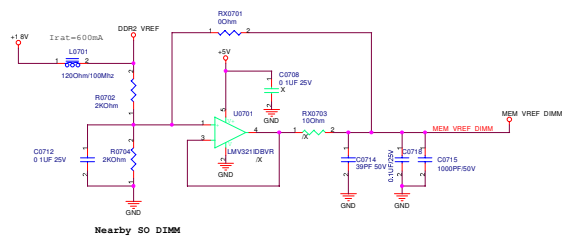
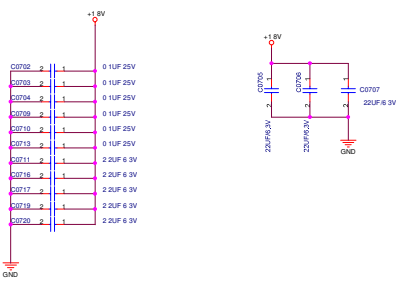
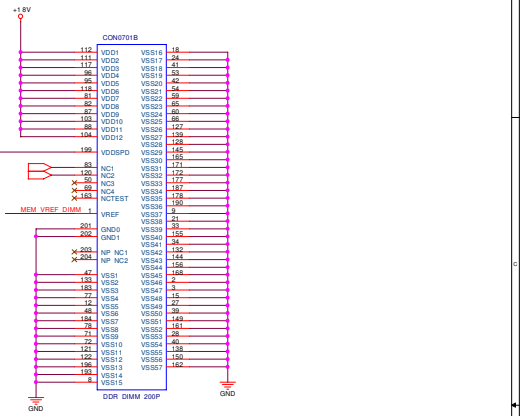
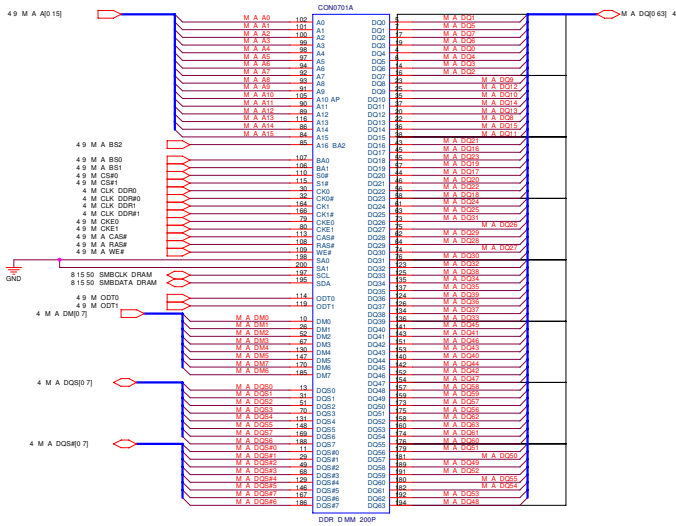
<http://konweer.kiev.ua>

« Kennedy_Zhang »

<http://konweer.kiev.ua>

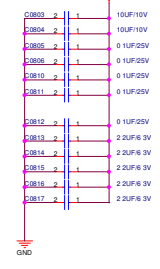
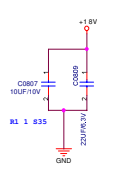
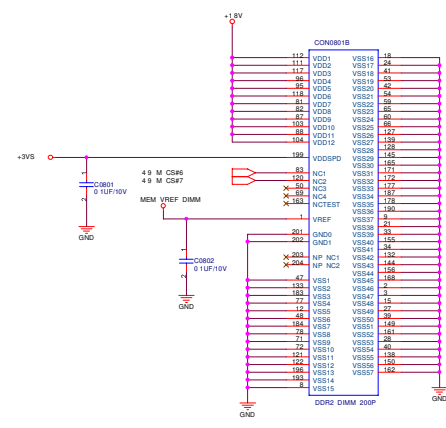
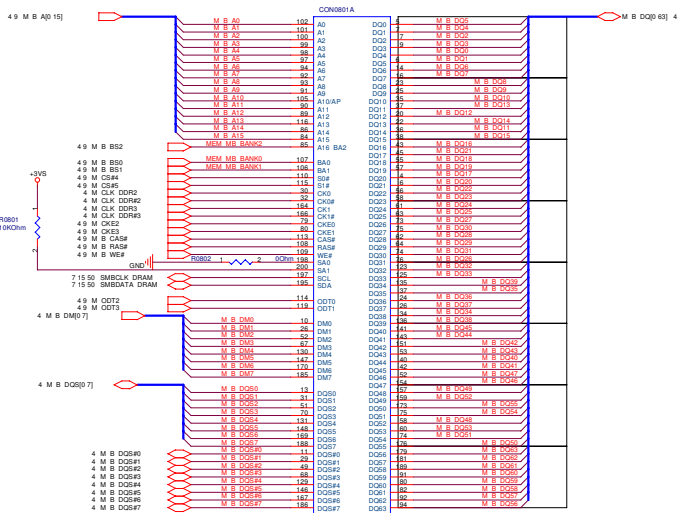
« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2012-03-29 10:07	Sheet: 6		of 26



<http://konweer.kiev.ua>

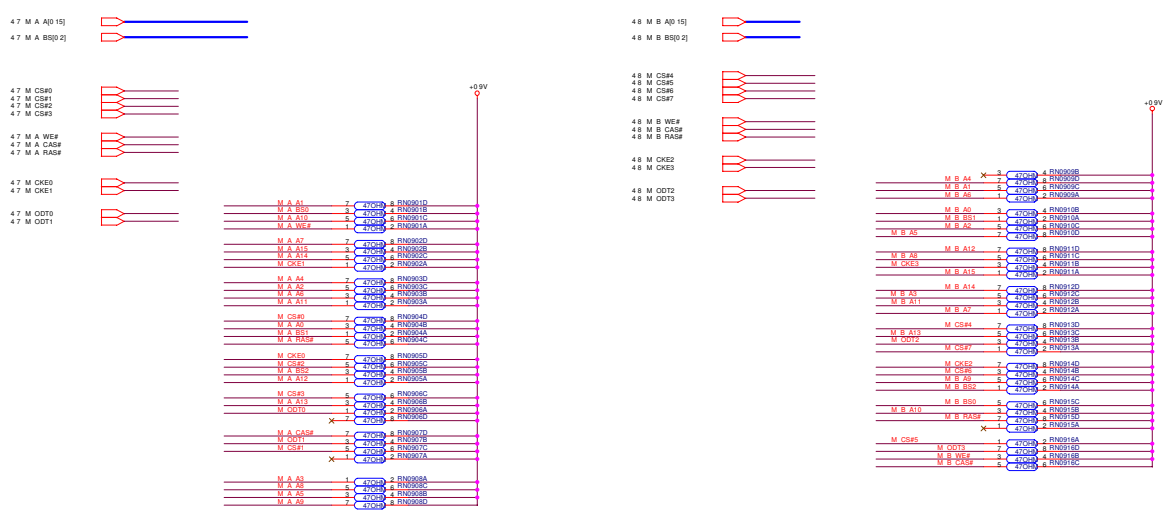
<< Kennedy_Zhang >>



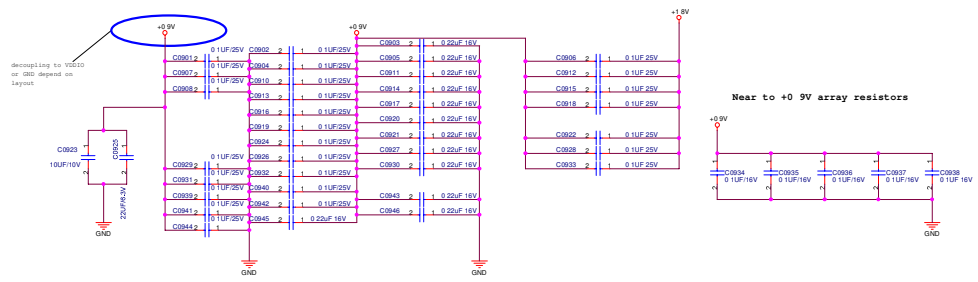
<http://konweer.kiev.ua>

« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTeK Computer INC.		Engineer: He Wang	
Rev	0	Rev	1.0
Date	08.10.2007	Sheet	6 of 8



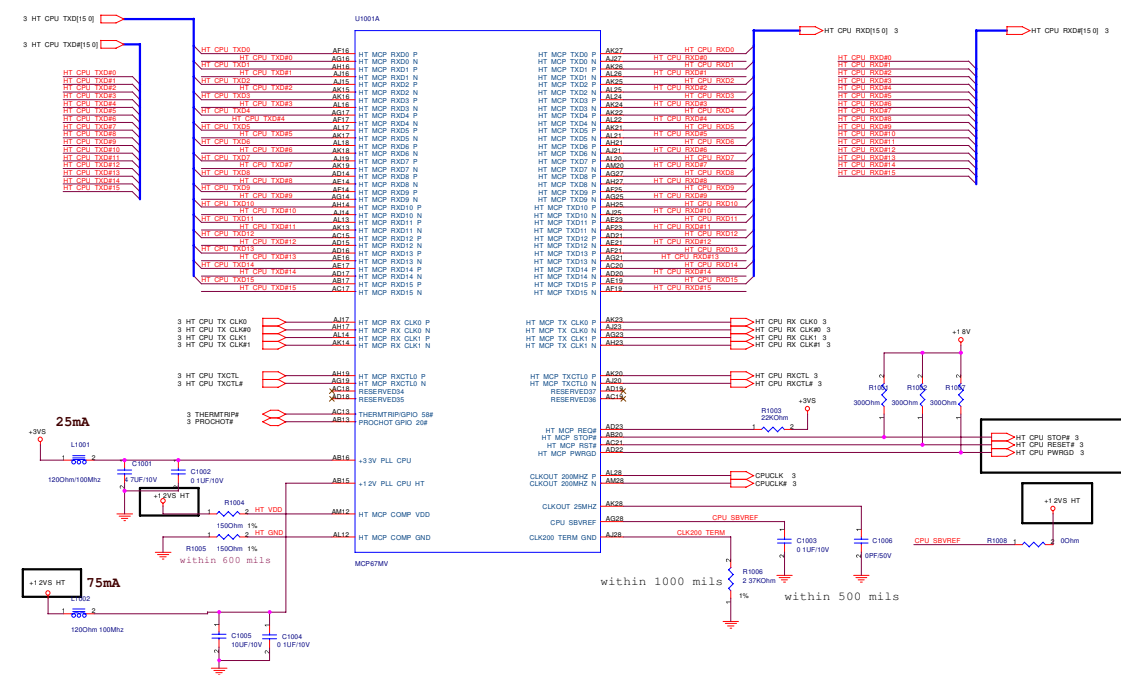
Layout Note: Place one cap close to every 2 pullup resistors terminated to +0.9V



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« Kennedy_Zhang »

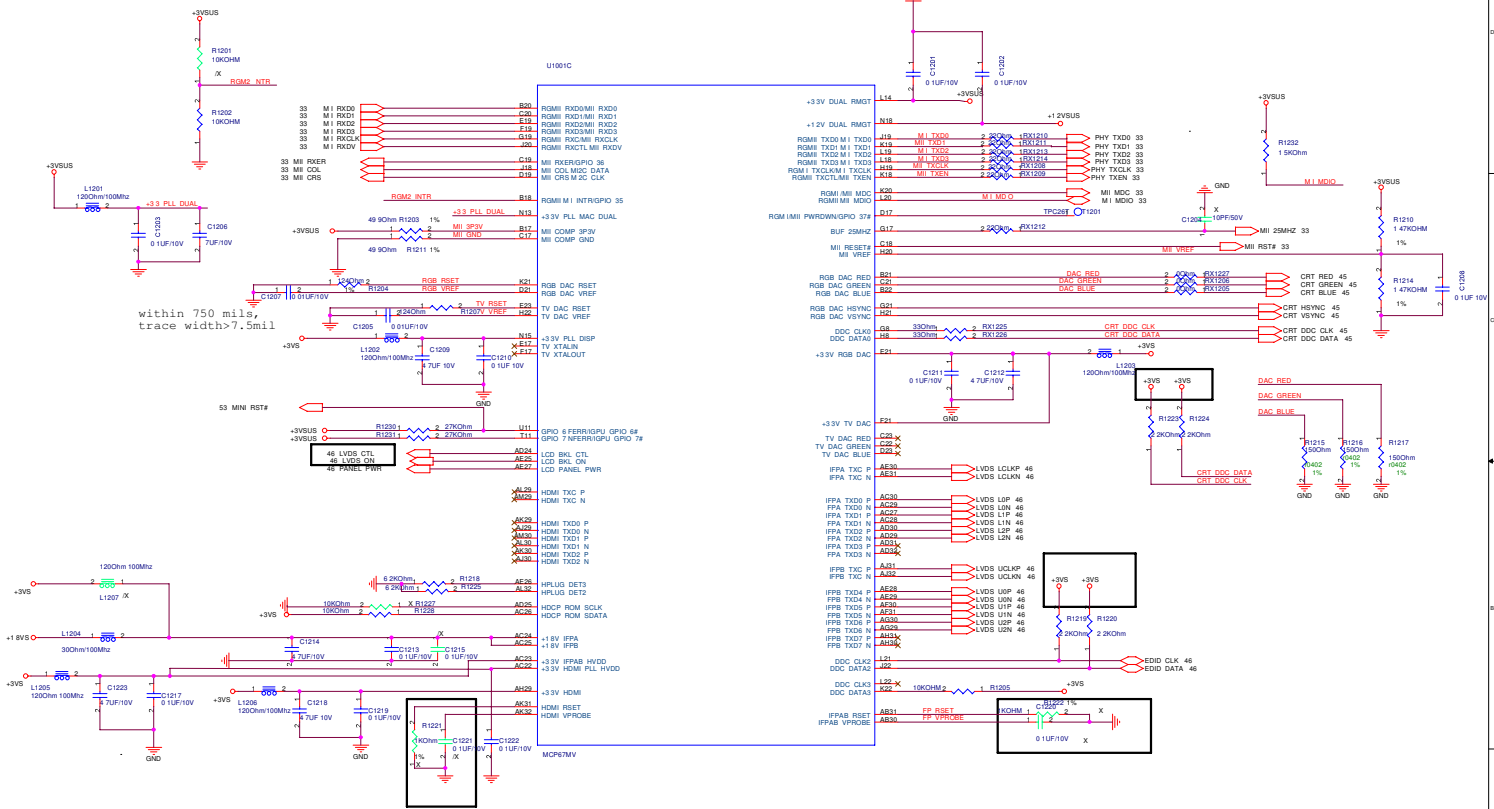
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ASUSTek Computer Inc.		Engineer: He Wang	
BSZ	Project Name	Rev	1.0
C	FSU	Rev	1.0
Date: 2011-01-19 10:07	Sheet: 8	of	88



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« Kennedy_Zhang »

		Title: FSU	
<small>ASUSTek Computer INC.</small>		Engineer: He Wang	
<small>BSZ</small> C	<small>Sheet Name</small> FSU	<small>Rev</small> 1.0	<small>Date</small> 10-27-2007
<small>Sheet</small> 10		<small>Total</small> 21	

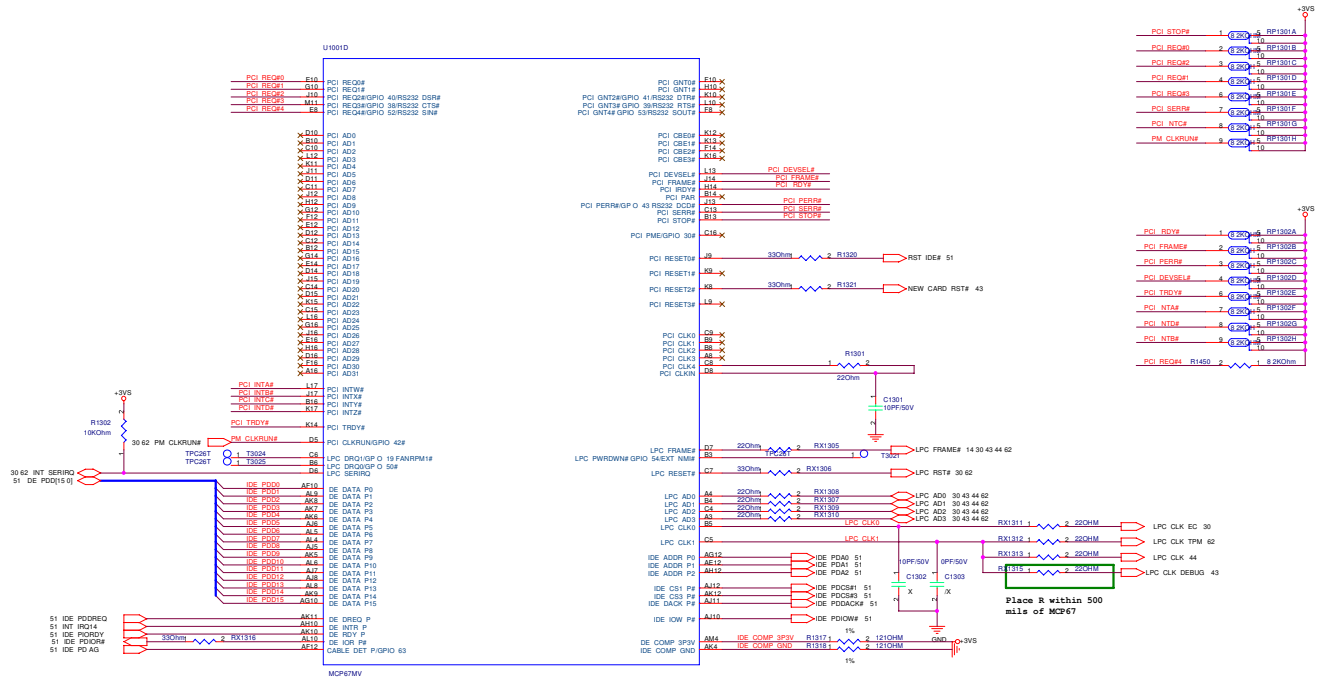


within 750 mils,
trace width > 7.5mil

<http://konweer.kiev.ua>

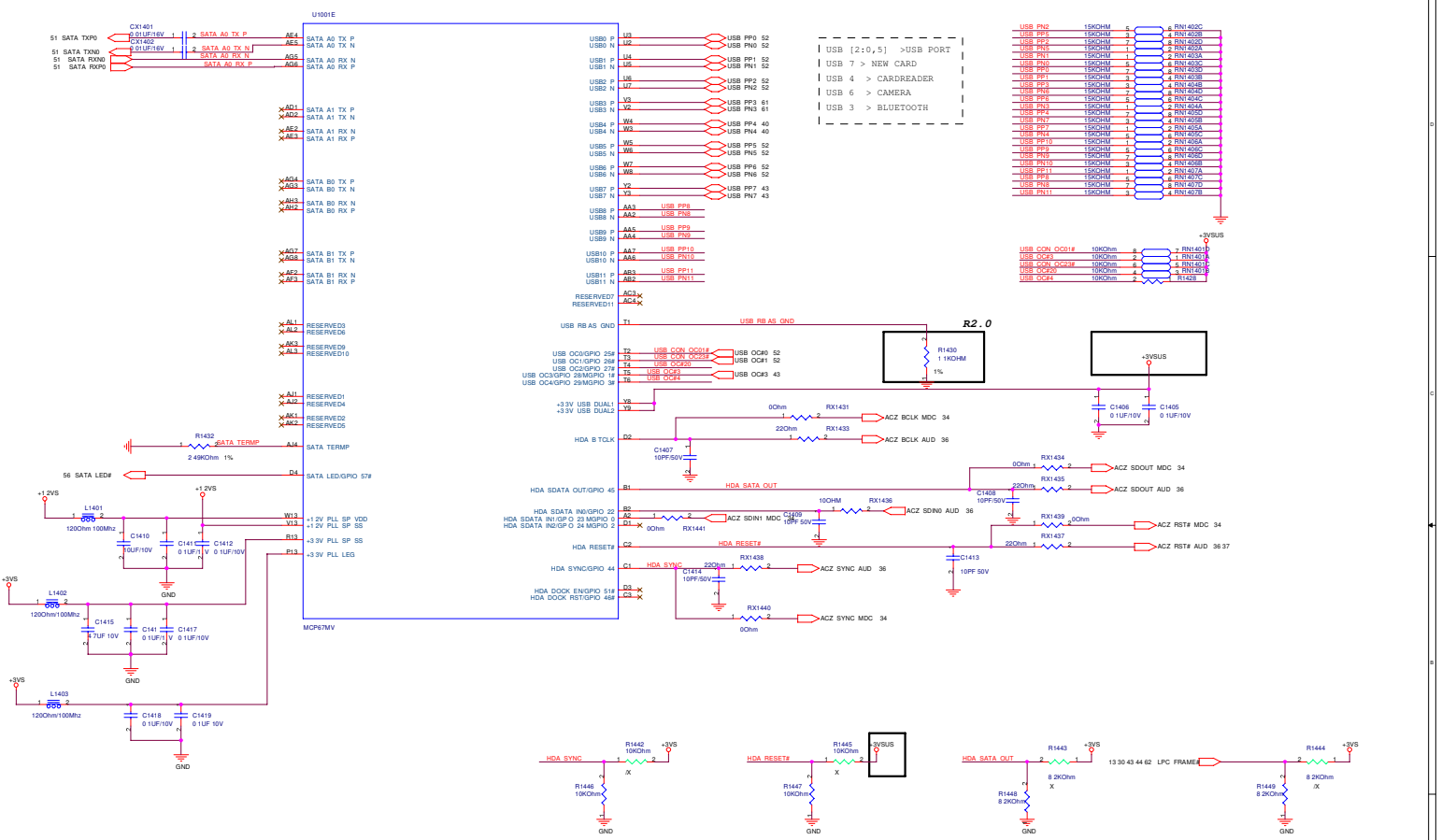
« Kennedy_Zhang »

ASUS		Title: FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
B24	P	Order Name	Rev
C		FSU	1.0
Date: 08-10-2007	Sheet: 15	of	26



<http://konweer.kiev.ua>

« Kennedy Zhang »

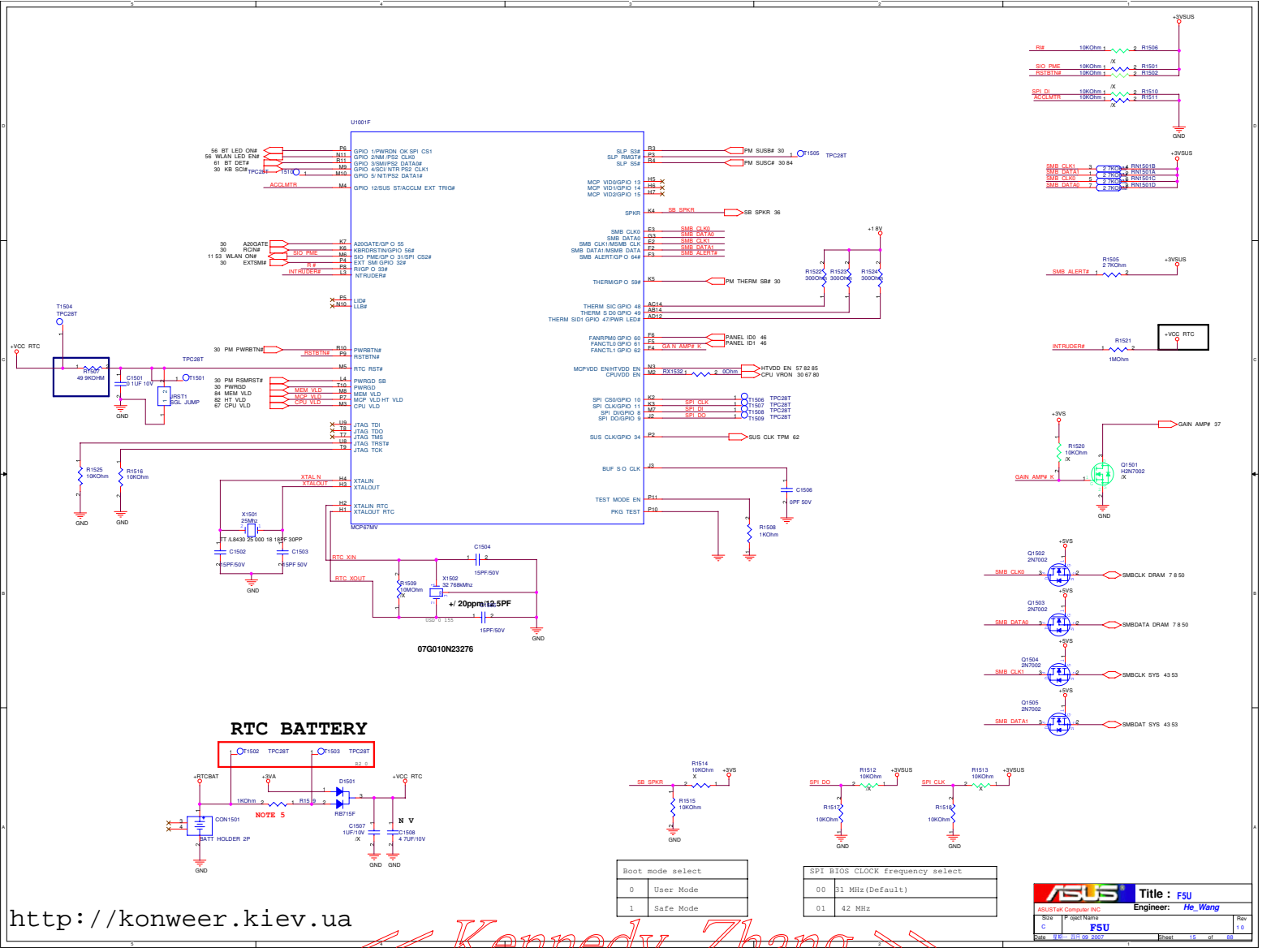


SIO CLOCK select		Networking select		BIOS Select	
0	1-4 31818 MHz (Default)	0	MI I	00	LPC BIOS
1	24 MHz	1	RG MII	01	PCI BIOS

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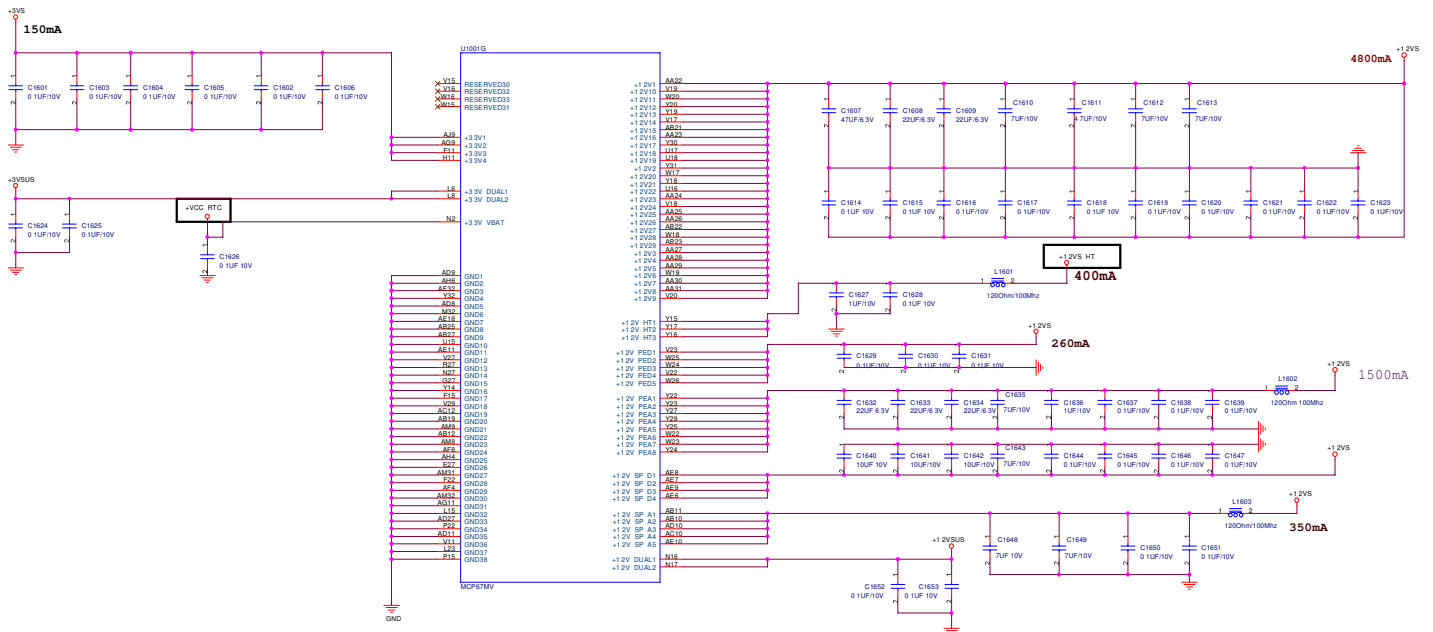
« Kennedy_Zhang »

ASUS Title: FSU
 ASUSTek Computer INC Engineer: He Wang
 Rev: 1.0
 Date: 08-19-2007 Sheet: 14 of 26



<http://konweer.kiev.ua>

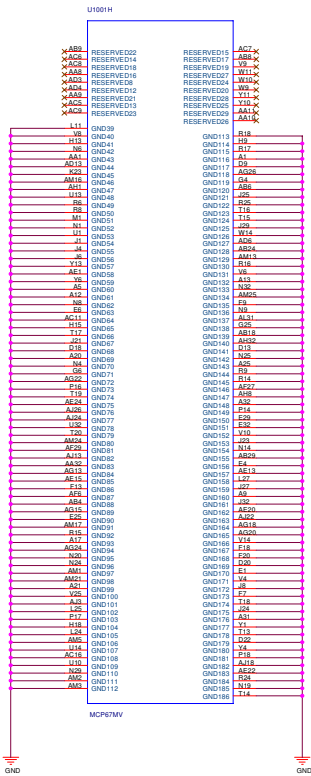
« Kennedy_Zhang »



<http://konweer.kiev.ua>

« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Rev	0	Rev	1.0
Date:	2012.09.20	Sheet:	16 of 26




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<< Kennedy_Zhang >>

		Title : FSU
ASUSTeK Computer INC		Engineer: He Wang
Size	Sheet Name	Rev
C	FSU	1.0
Date: 2015-05-25 10:07		Sheet: 17 of 28


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-05-29 10:07		Sheet: 18	of 28


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-05-29 10:07		Sheet: 18	of 28


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-11-28 2007		Sheet: 25 of 25	


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 21 of 28	


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2007-10-26 2007		Sheet: 25 of 25	


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 23 of 28	


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2007-10-26 2007		Sheet: 24 of 28	


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date	10/28/2007	Sheet	25 of 25


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date	10/28/2007	Sheet	25 of 25


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date	10-28-2007	Sheet	21 of 28


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2007-11-28 2007		Sheet: 25 of 25	

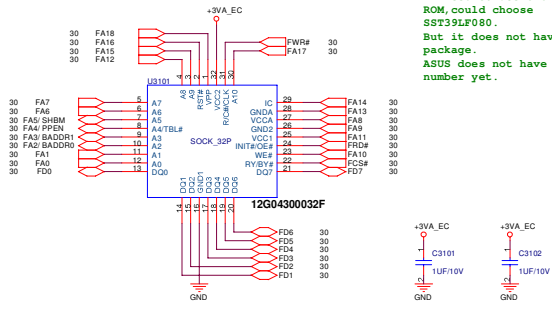
<http://konweer.kiev.ua>

« Kennedy_Zhang »

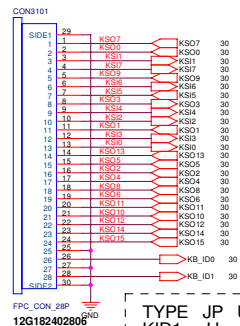
		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2007-10-26 2007		Sheet: 25 of 25	

ISA ROM (4Mbits)

SST-PLCC32 4Mbits Flash ROM
 PN:05G001014110
 (FLASH SST S3T39VF040-70-4C-NHE
 4M-70 PLCC-32)
 If need to use 8Mbits ISA
 ROM, could choose
 SST39LF080.
 But it does not have PLCC32
 package.
 ASUS does not have part
 number yet.



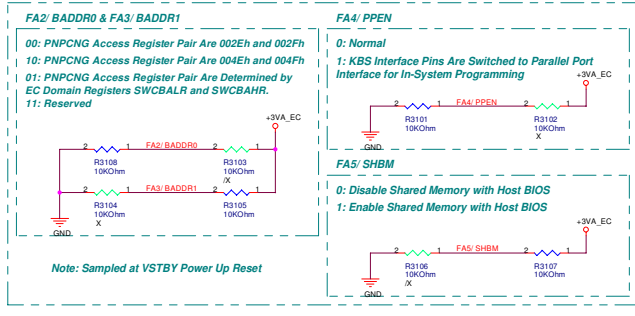
KBC connector



TYPE	JP	UK	US
KID1	H	H	L
KID2	L	H	L

ISA ROM

EC Hardware Strapping




ASUS		Title : FSU	
ASUS Tek Computer NC		Engineer: He_Wang	
Size	Project Name	Rev	
Custom	FSU	1.0	
Date: 11/11/2007	Sheet	31	of 38

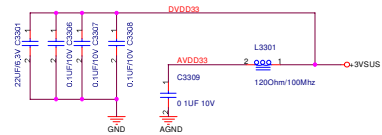
<http://konweer.kiev.ua>

<< Kennedy_Zhang >>

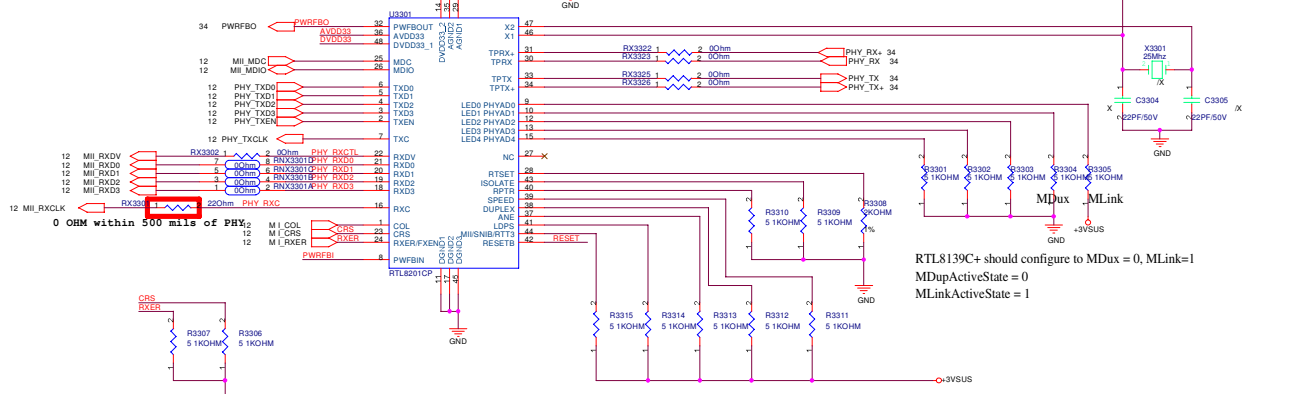
<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2012-05-29 10:07	Sheet: 01	of	01

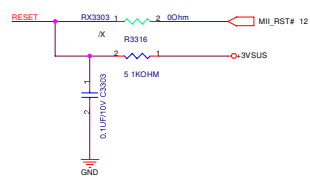
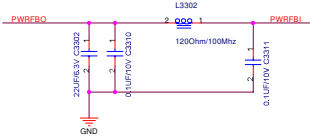


It should be RTL8201CL, 02G61120141



RTL8139C+ should configure to MDux = 0, MLink=1
 MDupActiveState = 0
 MLinkActiveState = 1

- ISOLATE: set high to isolate the RTL8201CP from the MAC. this will also isolate the MC/MDIO management interface
- RPTR: set high to put the RTL8201CP into repeater mode
- SPEED: this pin is latched to input during a power on or reset condition. set high to put the RTL8201CP into 100Mbps operation
- DUPLX: Set high to enable full duplex
- ANE: Set high to enable auto-negotiation mode, set low to force mode
- LDPS: set high to put the RTL8201CP into LDPS mode
- MII/SNIB: pull high to set the RTL8201CP into MII mode operation. set low for SNI mode

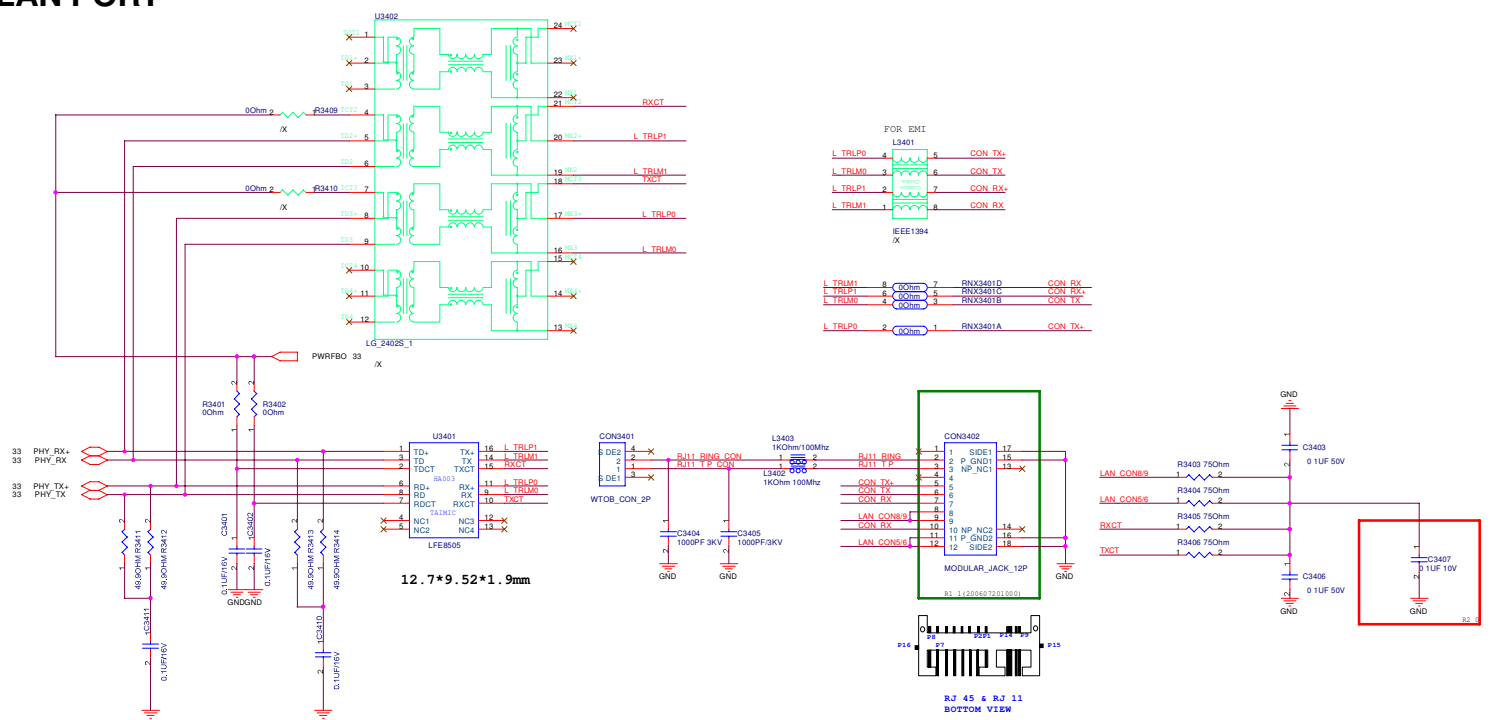


<http://konweer.kiev.ua>

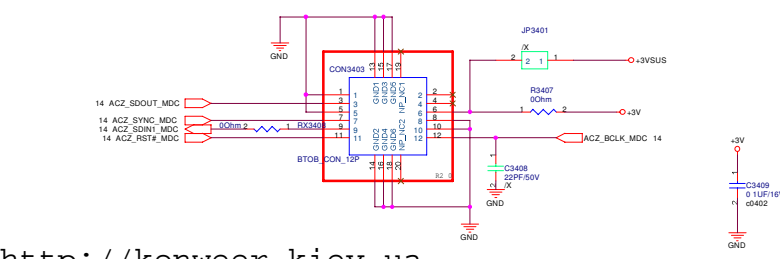
ASUS		Title : FSU	
ASUSTek Computer, Inc.	Project Name	Engineer: He_Wang	Rev
Size	Custom	FSU	1.0
Date: 11/11/09	2009	Sheet	33 of 38

<< Kennedy_Zhang >>

LAN PORT



MDC CONN.



<http://konweer.kiev.ua>

ASUS		Title : FSU	
ASUSTek Computer INC	Project Name	Engineer: He_Wang	Rev
Custom	FSU		1.0
Date: 11/11/09 2:07	Sheet 34	of 38	

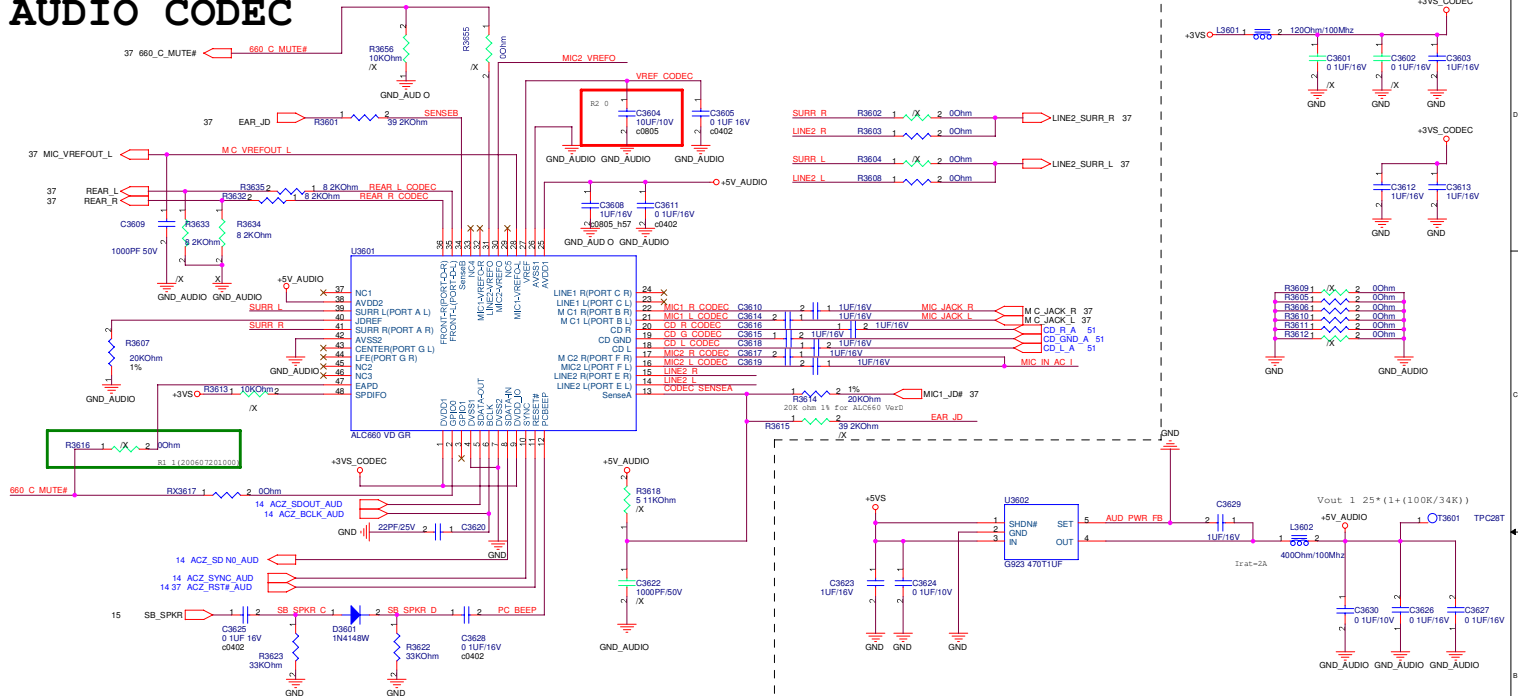
<< Kennedy_Zhang >>

<http://konweer.kiev.ua>

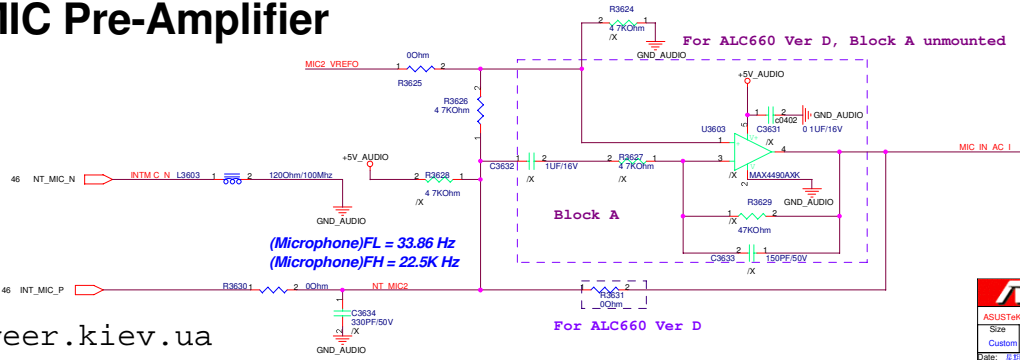
		Title : FSU	
ASUS Tek Computer NC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date: 08-11-28-2007		Sheet 05 of 08	

<< Kennedy_Zhang >>

AUDIO CODEC



Internal MIC Pre-Amplifier

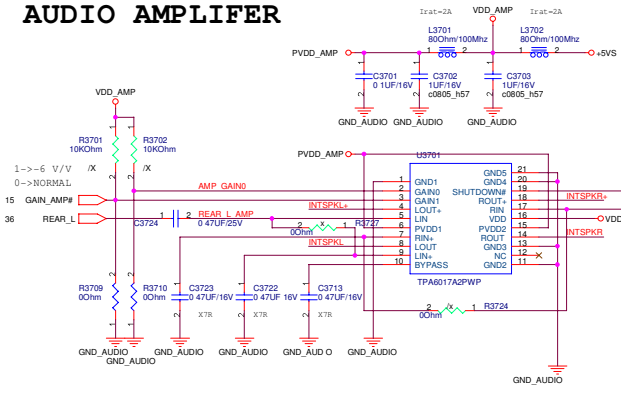


ASUS		Title : FSU	
ASUSTeK Computer INC		Engineer: He_Wang	
Size	Project Name	Date	Rev
Custom	FSU	11/11/09 2007	1.0
Date		Sheet	36 of 38

<http://konweer.kiev.ua>

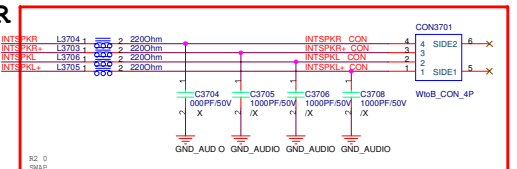
<< Kennedy_Zhang >>

AUDIO AMPLIFIER

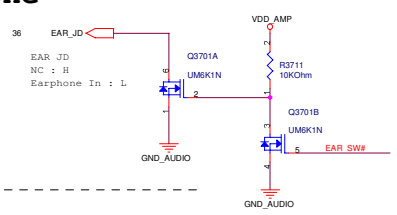


SPEAKER

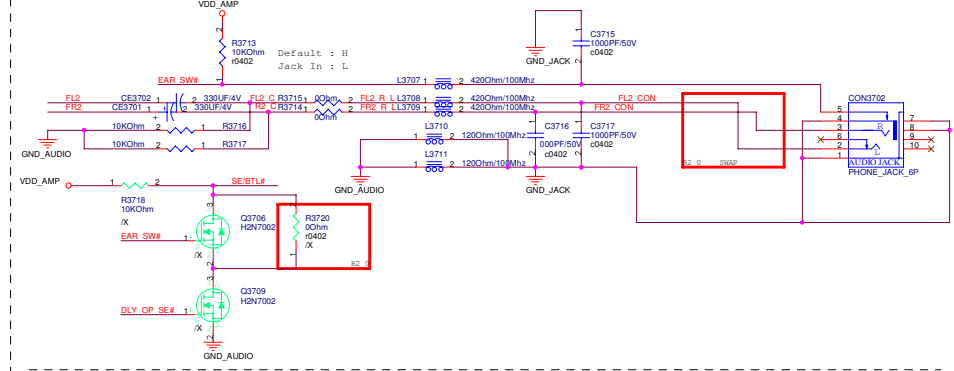
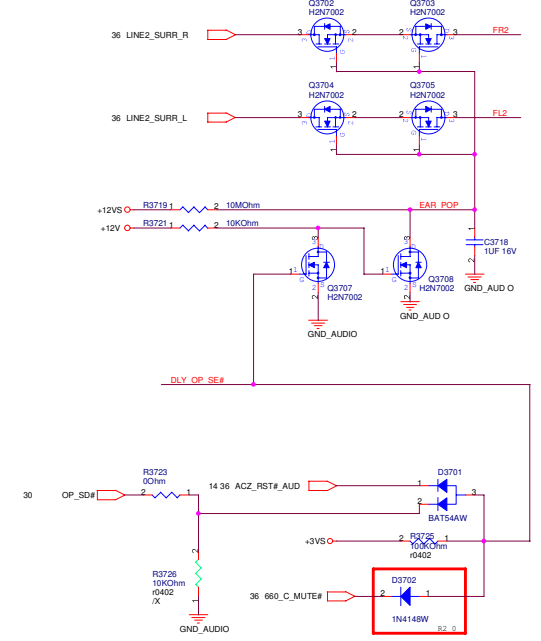
To Internal Speaker Connector



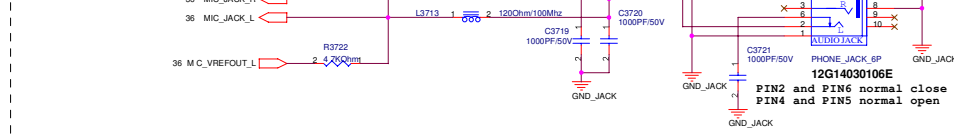
HeadPhone



DEPOP CIRCUIT



External Microphone



ASUS Title : FSU
 ASUSTek Computer INC Engineer: He_Wang
 Size Project Name
 Custom FSU
 Date: 11/18/2007 Sheet 37 of 38

<http://konweer.kiev.ua>

<< Kennedy_Zhang >>


<http://konweer.kiev.ua>

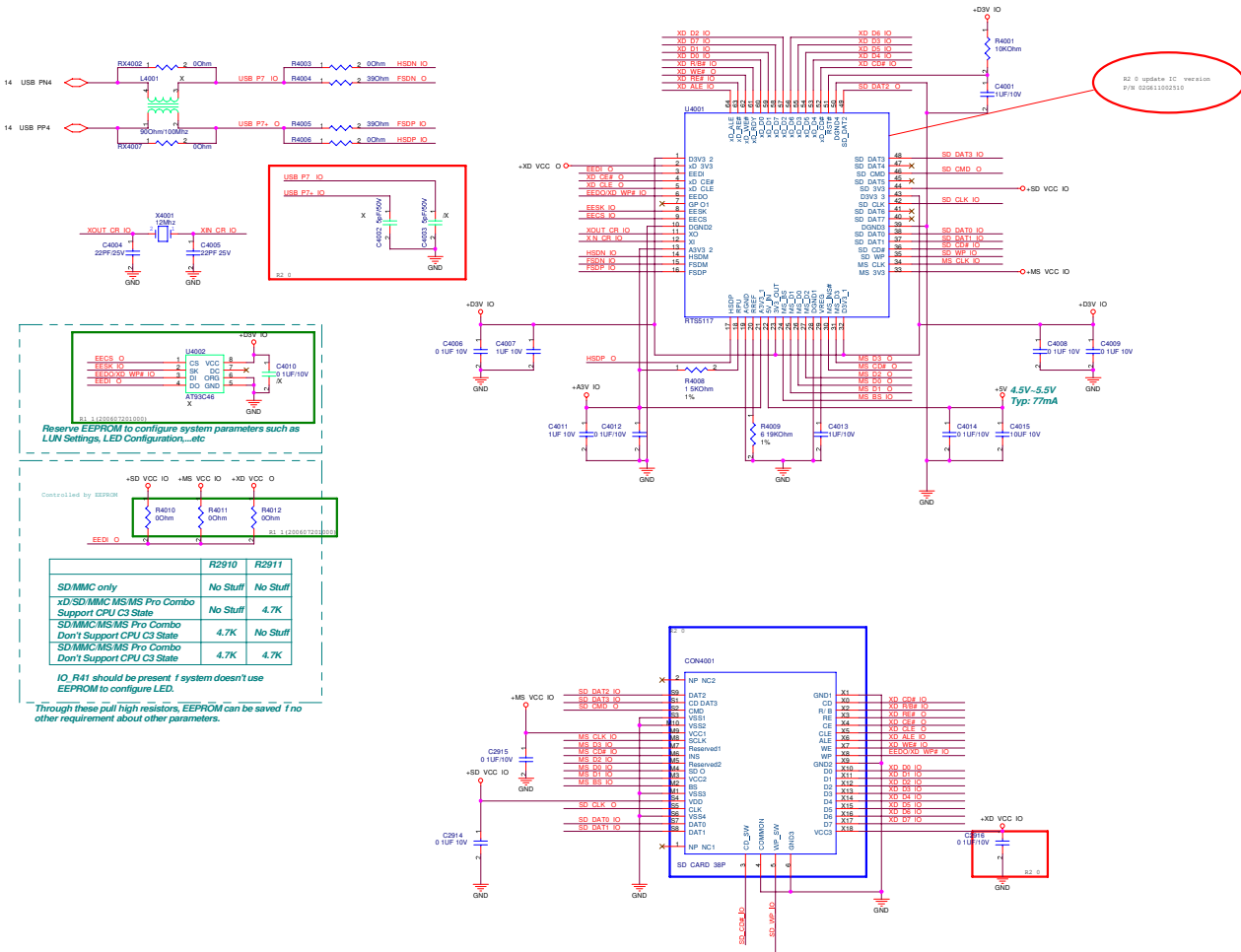
		Title : FSU	
ASUSTek Computer INC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date:	2007-11-28	Sheet	38 of 38

<< Kennedy_Zhang >>

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« Kennedy_Zhang »


		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Object Name	Rev	
C	FSU	1.0	
Date: 2012-03-29 10:07	Sheet: 01		of 01



<http://konweer.kiev.ua>

« Kennedy_Zhang »

<http://konweer.kiev.ua>

		Title : FSU	
ASUSTeK Computer INC		Engineer: He_Wang	
Size	Project Name	Rev	
Custom	FSU	1.0	
Date: 11/23/2007	Sheet	41	of 88

<< Kennedy_Zhang >>

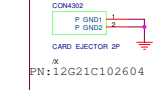
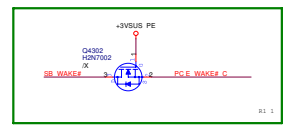
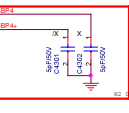
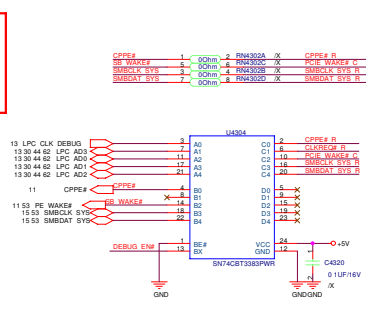
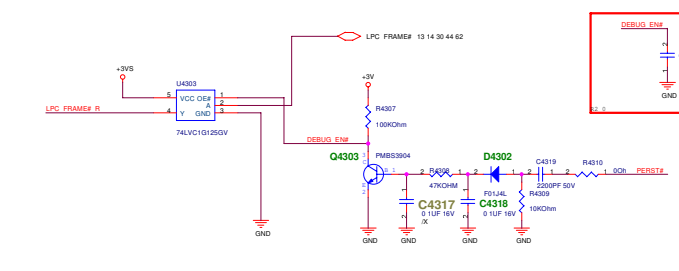
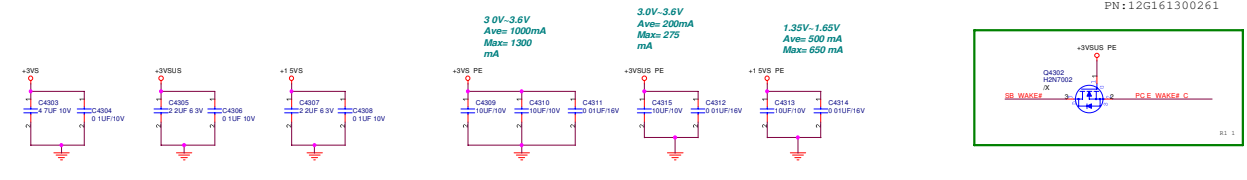
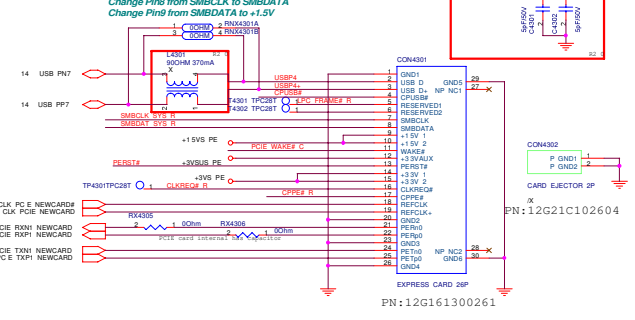
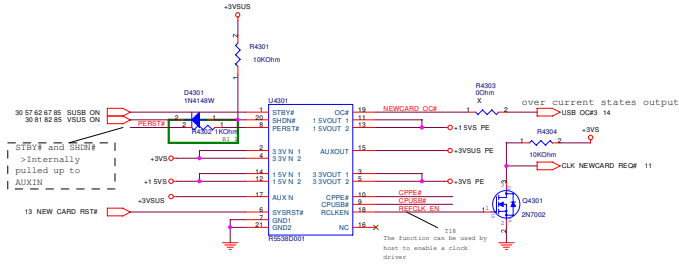
<http://konweer.kiev.ua>

		Title : FSU	
ASUSTeK Computer INC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date: 11/28/2007		Sheet	42 of 68

<< Kennedy_Zhang >>

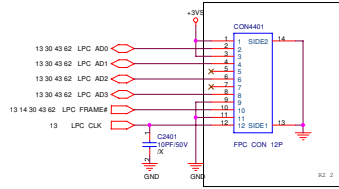
NEW CARD SOCKET

!! ExpressCard Standard 1.0:
Change Pin7 from RESERVED to SMBCLK
Change Pin8 from SMBCLK to SMBDATA
Change Pin9 from SMBDATA to +1.5V



PN:12G21C102604
EXPRESS CARD 28P
PN:12G161300261

LPC DEBUG CONNECTOR

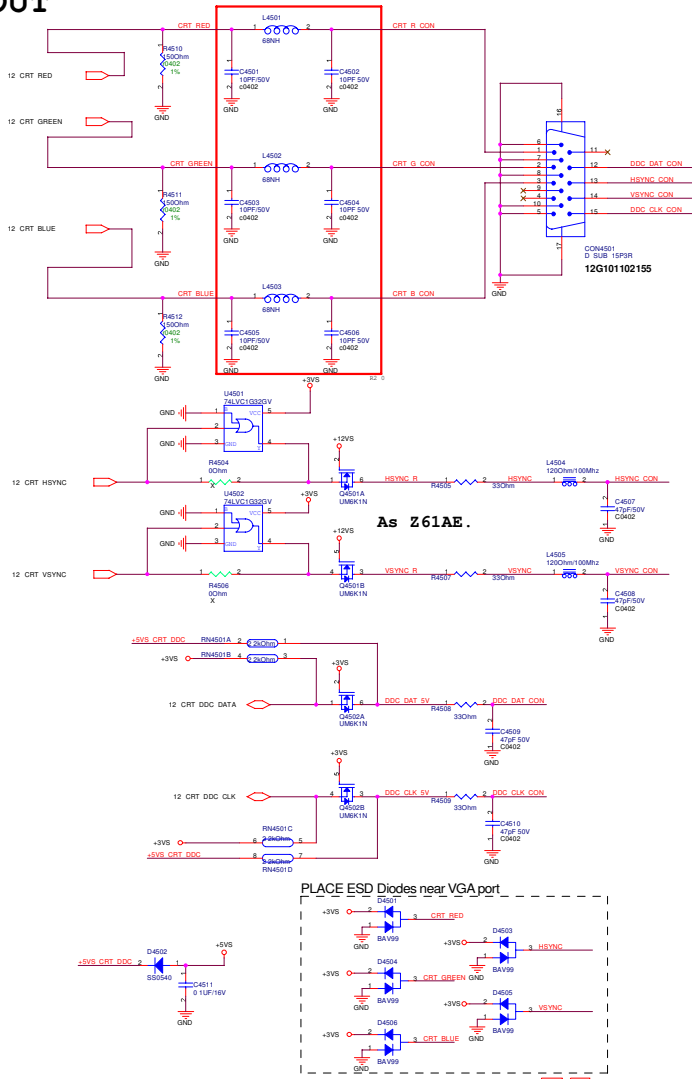


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Object Name	Rev	
C	FSU	1.0	
Date: 2011-09-23 10:07	Sheet: 44	of	88

VGA OUT

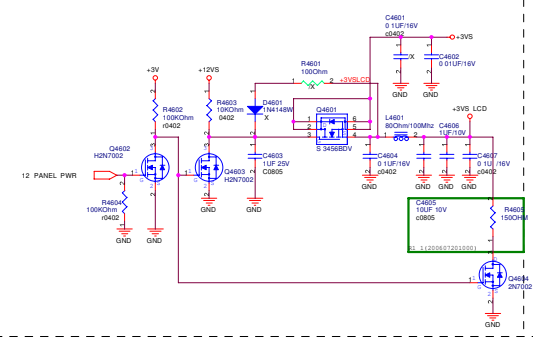


<http://konweer.kiev.ua>

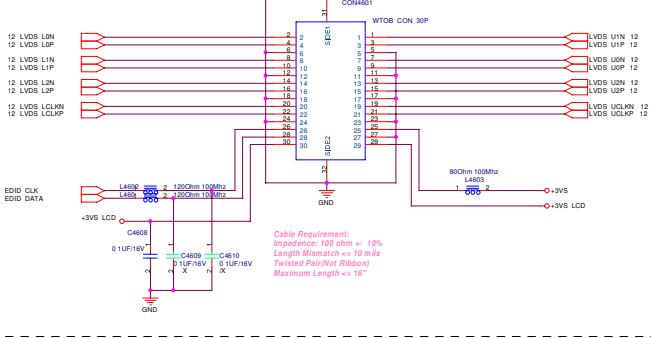
« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Rev	0	Part Name	
C		FSU	1.0
Date	08-19-2007	Sheet	45 of 48

LCD Power

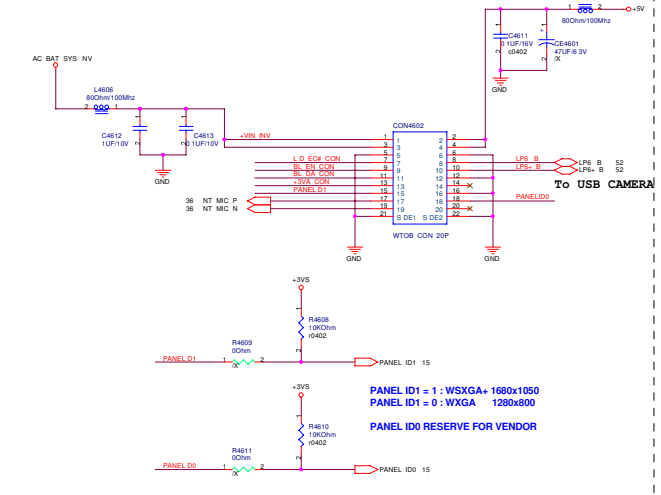


LCD LVDS Interface



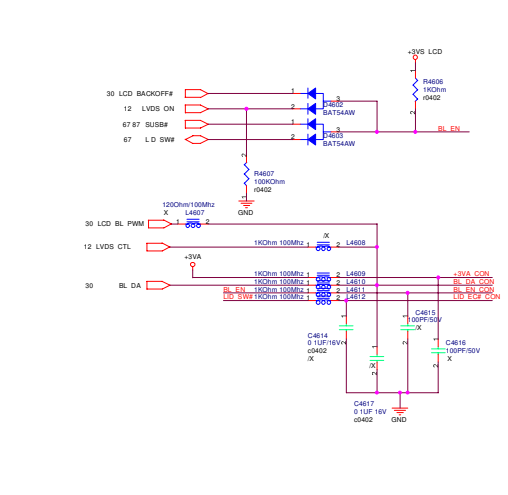
Cable Requirement:
 Impedance: 100 ohm +/- 10%
 Length Mismatch <= 10 mils
 Twisted Pair/Wire Ribbons
 Maximum Length <= 15"

INVERTER Interface



PANEL ID1 = 1 : WSXGA+ 1680x1050
 PANEL ID1 = 0 : WXGA 1280x800
 PANEL ID0 RESERVE FOR VENDOR


Backlight Enable



<http://konweer.kiev.ua>

« Kennedy_Zhang »


<http://konweer.kiev.ua>

		Title : FSU	
ASUSTek Computer INC		Engineer: He_Wang	
Size	Project Name		Rev
Custom	FSU		1.0
Date	2007-08-28	Sheet	47 of 88

<< Kennedy_Zhang >>


<http://konweer.kiev.ua>

« Kennedy_Zhang »

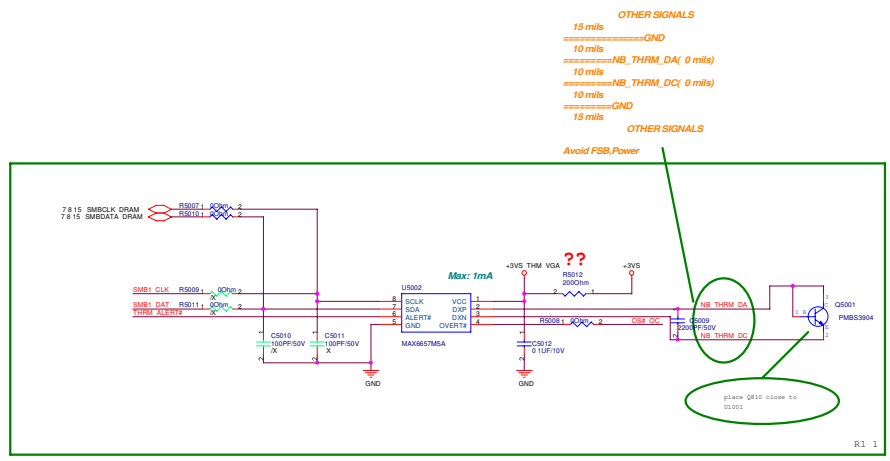
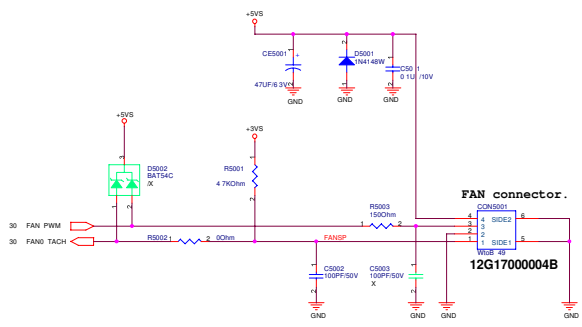
		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Object Name		Rev
C	FSU		1.0
Date: 2011-01-20 10:07		Sheet: 41	of 48

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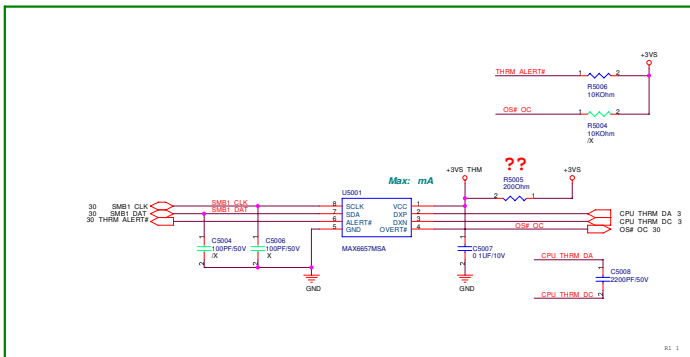
« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-03-29 10:07		Sheet: 41	of 48

DC FAN control



Thermal Sensor



Route CPU_THRM_DA and CPU_THRM_DC on the same layer

OTHER SIGNALS

15 mils

=====GND

10 mils

=====H_THERMDA(10 mils)

10 mils

=====H_THERMDC(10 mils)

10 mils

=====GND

15 mils

OTHER SIGNALS

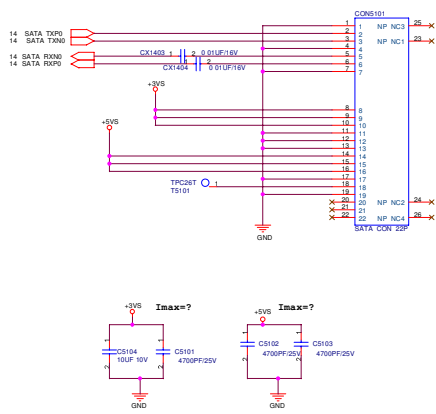
Avoid FSB, Power

<http://konweer.kiev.ua>

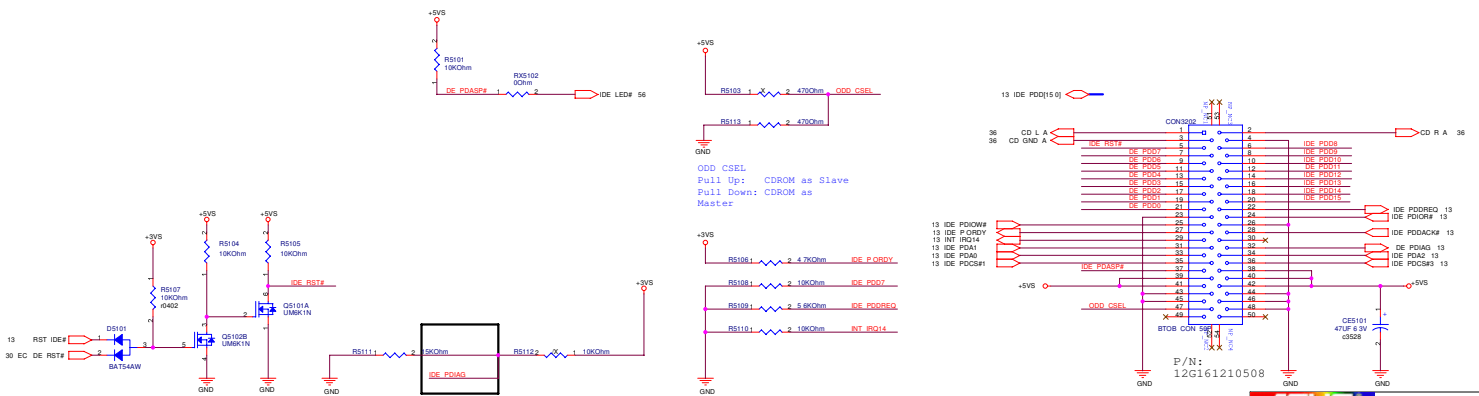
« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC		Engineer: He_Wang	
Doc	P	Project Name	Rev
C		FSU	1.0
Doc No	101108-2007	Sheet	30 of 38

SATA_HDD(Default)



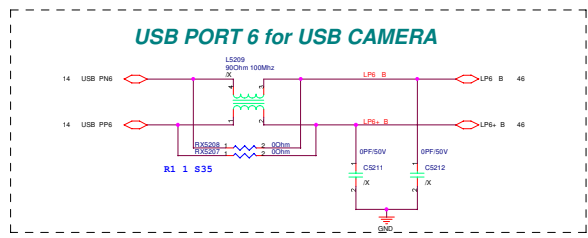
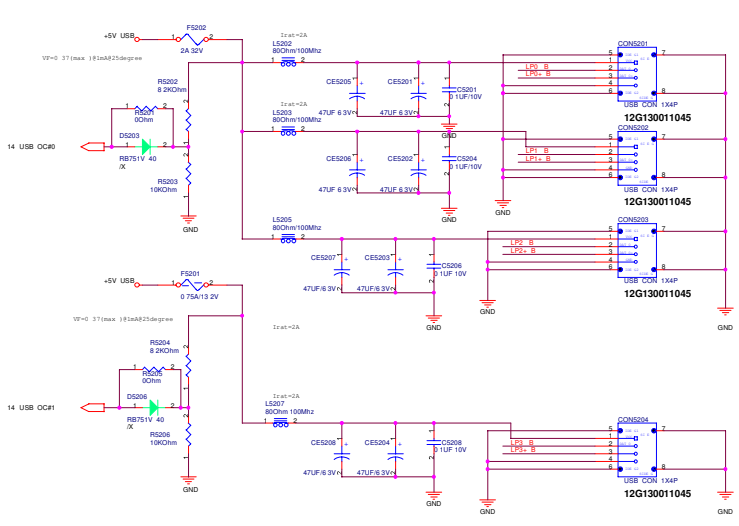
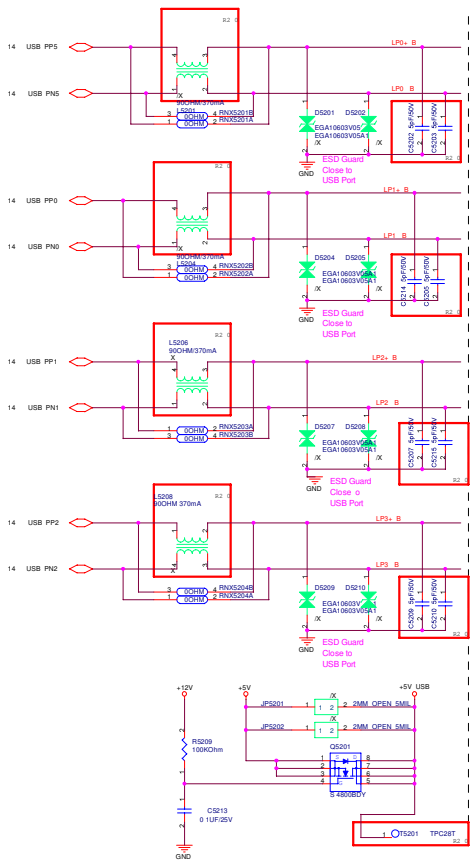
CD-ROM



ASUS		Title : FSU	
ASUSTek Computer INC		Engineer: He Wang	
Rev	FSU	Rev	1.0
Date: 06-10-2007		Sheet: 51	of 58

<http://konweer.kiev.ua>

« Kennedy_Zhang »

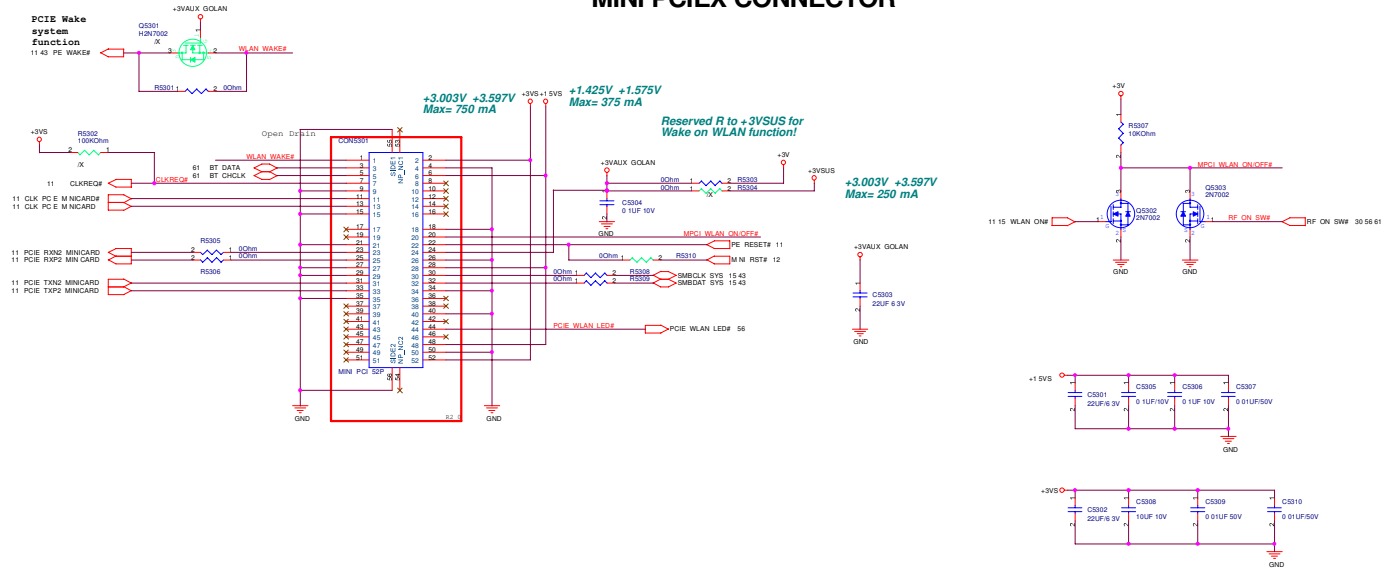


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
<small>ASUSTek Computer Inc.</small>		<small>Engineer: He Wang</small>	
<small>Rev</small>	<small>Order Name</small>	<small>Rev</small>	<small>Rev</small>
<small>1</small>	<small>FSU</small>	<small>1</small>	<small>1.0</small>
<small>Date: 2011-05-19 10:07</small>	<small>Sheet: 52</small>	<small>of</small>	<small>58</small>

MINI PCIEX CONNECTOR




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« Kennedy_Zhang »

ASUS		Title : FSU	
ASUS Tech Connector NIC		Engineer: He Wang	
Rev	Doc Name	Rev	
1	FSU	1.0	
Date: 2011-01-20 10:07	Sheet: 01	of	08


<http://konweer.kiev.ua>

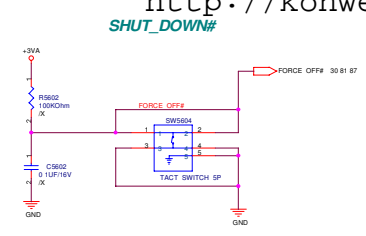
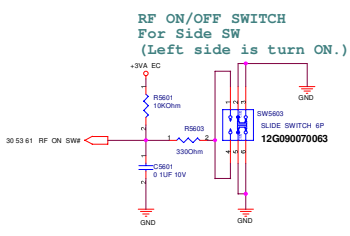
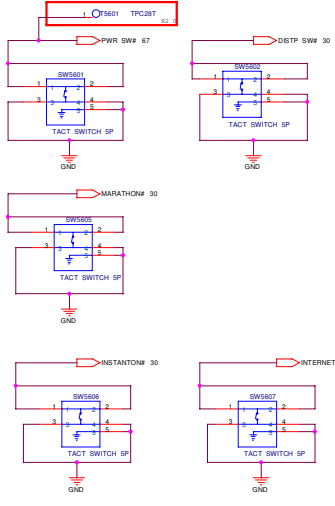
« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Object Name		Rev
C	FSU		1.0
Date: 2011-01-20 10:00		Sheet: 54	of 55

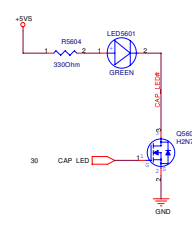
<http://konweer.kiev.ua>

« Kennedy_Zhang »

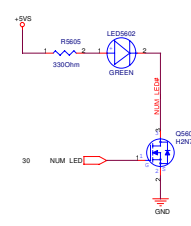
		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2012-05-29 10:07	Sheet: 55	of	55



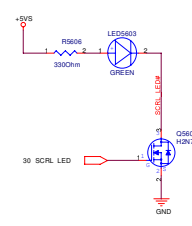
for Cap Lock



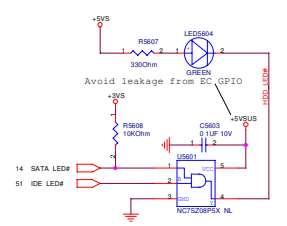
for Num Lock



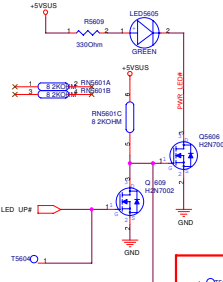
for Scroll Lock



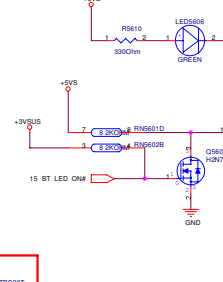
For SATA/IDE LED



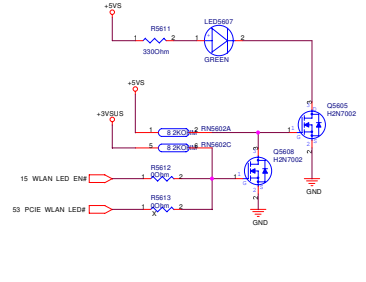
For POWER LED



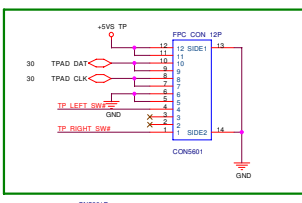
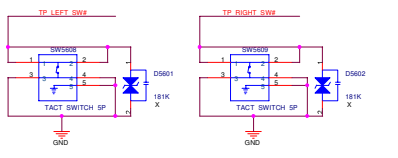
For BT LED



For WireLess LED

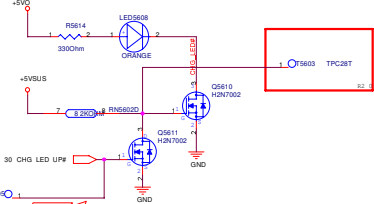


TOUCH PAD SWITCH



- TP RIGHT SW# 2 10kOhm R5601
- TP LEFT SW# 2 10kOhm R5601
- TPAD SW# 2 10kOhm R5601
- TPAD CLK 2 10kOhm R5601

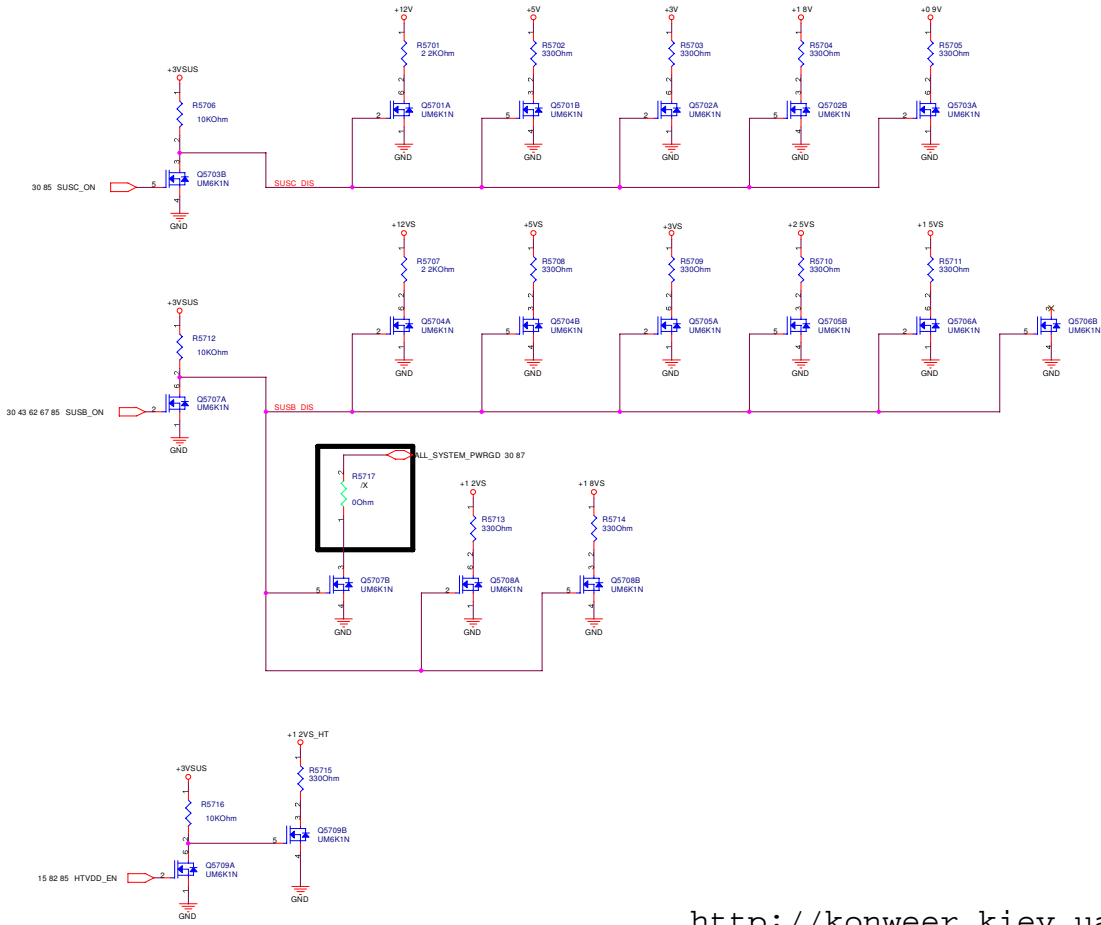
For BATTERY LED



« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Rev	1.0	Rev	1.0
Date: 2008-08-29	Sheet: 55	Date:	55

DISCHARGE CIRCUIT




<http://konweer.kiev.ua>

ASUS		Title : FSU
ASUSTek Computer, Inc.		Engineer: He_Wang
Size	Project Name	Rev
Custom	FSU	1.0
Date	2007.09.20	Sheet 37 of 38

<< Kennedy_Zhang >>


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« Kennedy_Zhang »

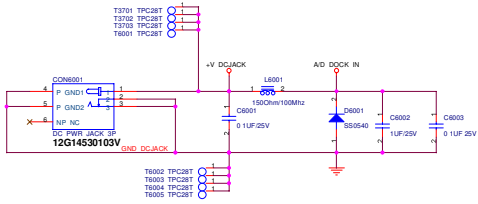
		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Object Name	Rev	
C	FSU	1.0	
Date: 2012-01-26 10:07	Sheet: 01	of	01

<http://konweer.kiev.ua>

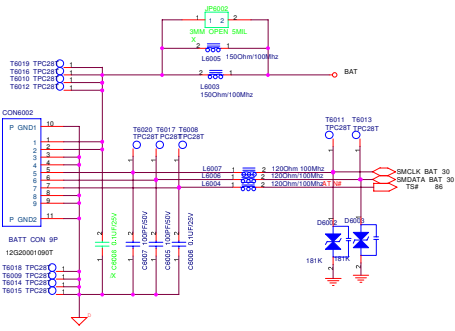
« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Object Name		Rev
C	FSU		1.0
Date: 2012-03-29 10:07		Sheet: 01	of 01

Adaptor IN Circuit



BATTERY IN CIRCUIT

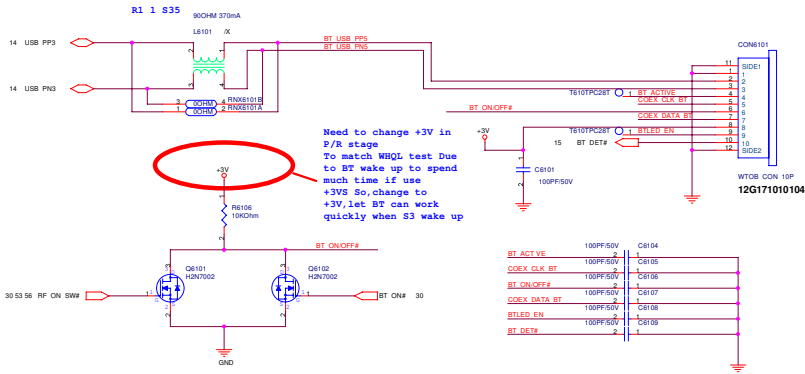


<http://konweer.kiev.ua>

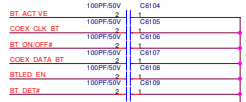
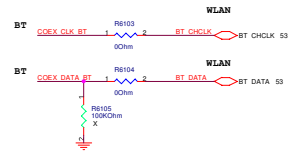
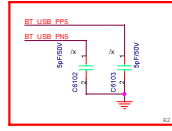
« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Rev	0	Objet Name	FSU
Date	01-19-2007	Sheet	61 of 68

BLUETOOTH CONNECTOR



Signal direction
CLK: BT > WLAN;
DATA: WLAN > BT

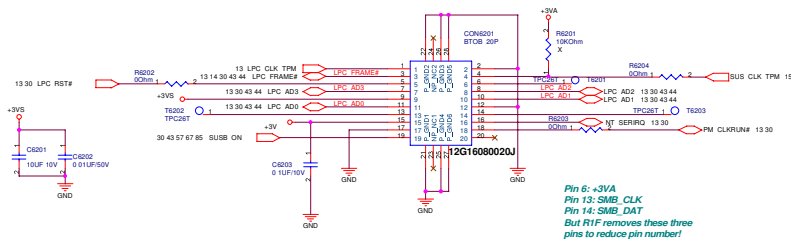


<http://konweer.kiev.ua>

« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC		Engineer: He Wang	
Size	Sheet Name	Rev	
C	FSU	1.0	
Date: 2011-01-29 10:07	Sheet: 61	of	68

TPM CONN.



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« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Sheet Name	Rev	
C	FSU	1.0	
Date: 2011-01-20 10:07	Sheet: 61	of	68


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« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Object Name	Rev	
C	FSU		1.0
Date: 2012-01-26 10:07	Sheet: 01	of	01


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« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-03-29 10:07		Sheet: 64	of 68


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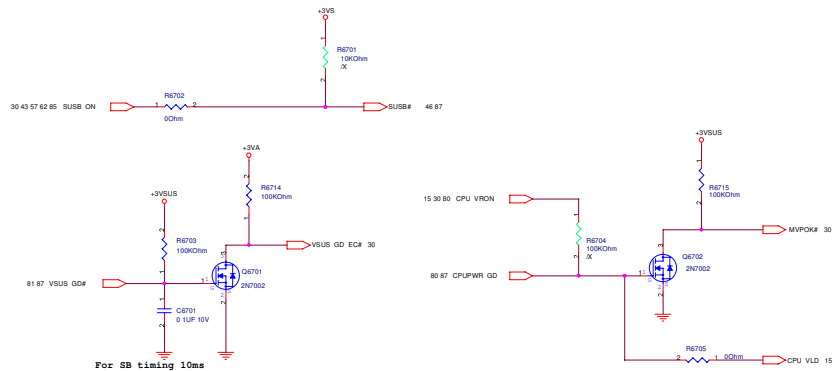
« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2011-01-20 10:07		Sheet: 65	of 66

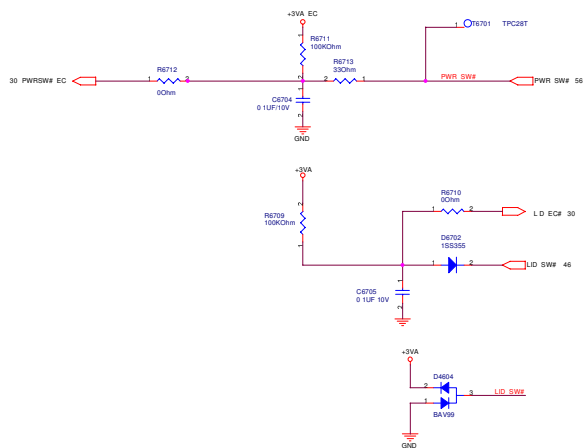
<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Object Name	Rev	
C	FSU	1.0	
Date: 2012-03-29 10:07	Sheet: 65		of 65



POWER SWITCH

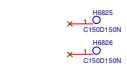
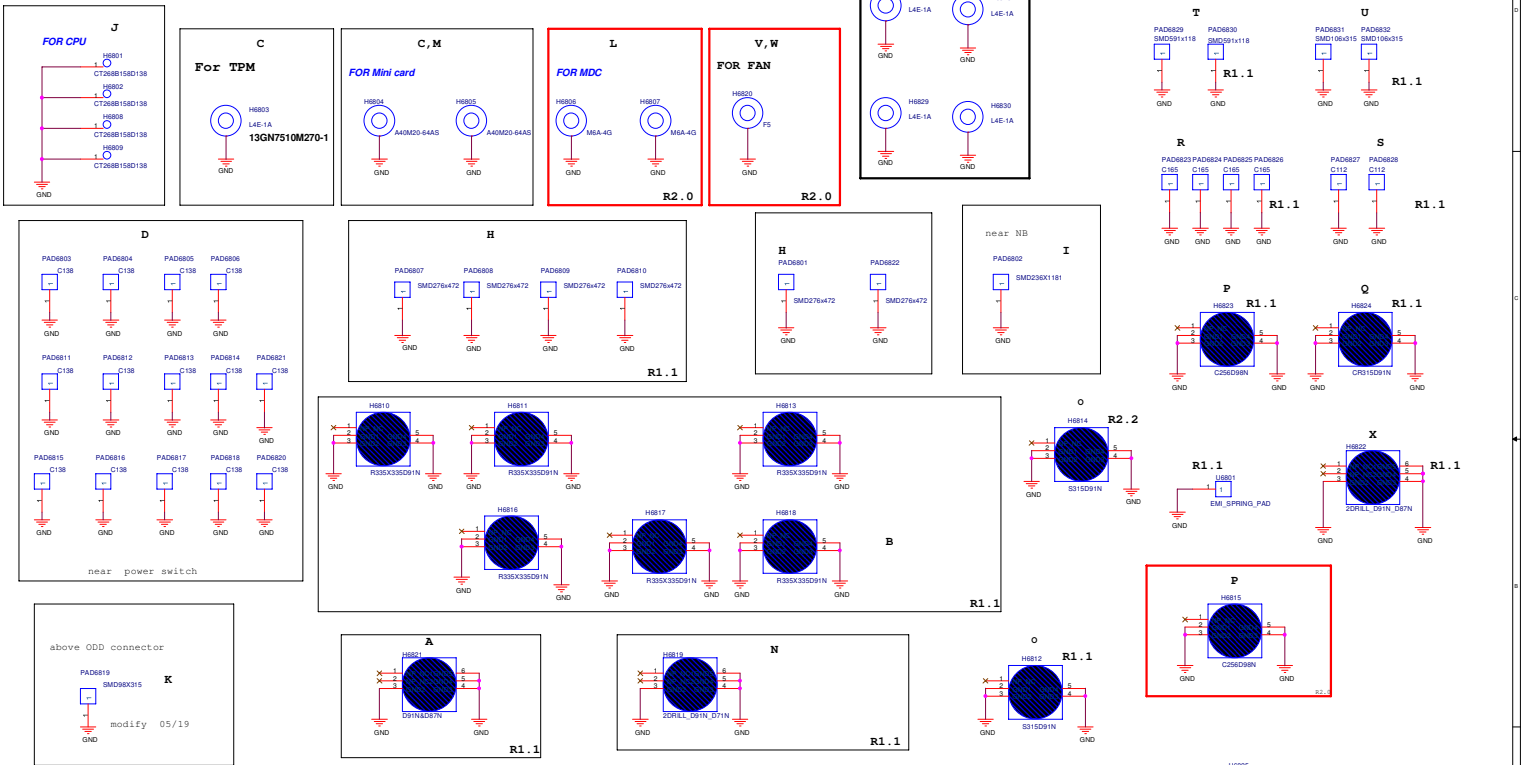


<http://konweer.kiev.ua>

« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC.		Engineer: He Wang	
Size	Sheet Name	Rev	
C	FSU	1.0	
Date: 2011-01-29 10:07	Sheet: 67	of	88

FOR Thermal
R2.0




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« Kennedy_Zhang »

ASUS		Title : FSU	
ASUSTek Computer INC		Engineer: He Wang	
B24	Project Name		Rev
C	FSU		1.0
Date: 2011-01-25 10:07	Sheet: 68	of	68


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« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2012-01-26 10:07	Sheet: 01	of	01


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« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-03-29 10:07		Sheet: 01	of 01


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« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2007-10-26 2007		Sheet: 11 of 28	


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« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date	10/28/2007	Sheet	15 of 25


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« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date	10/28/2007	Sheet	11 of 28


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« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 14 of 28	


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« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2007-10-26 2007		Sheet: 15 of 25	


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« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 20:07		Sheet: 16 of 28	


<http://konweer.kiev.ua>

« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name	Rev	
C	FSU	1.0	
Date: 2007-10-26 2007		Sheet: 11 of 28	


<http://konweer.kiev.ua>

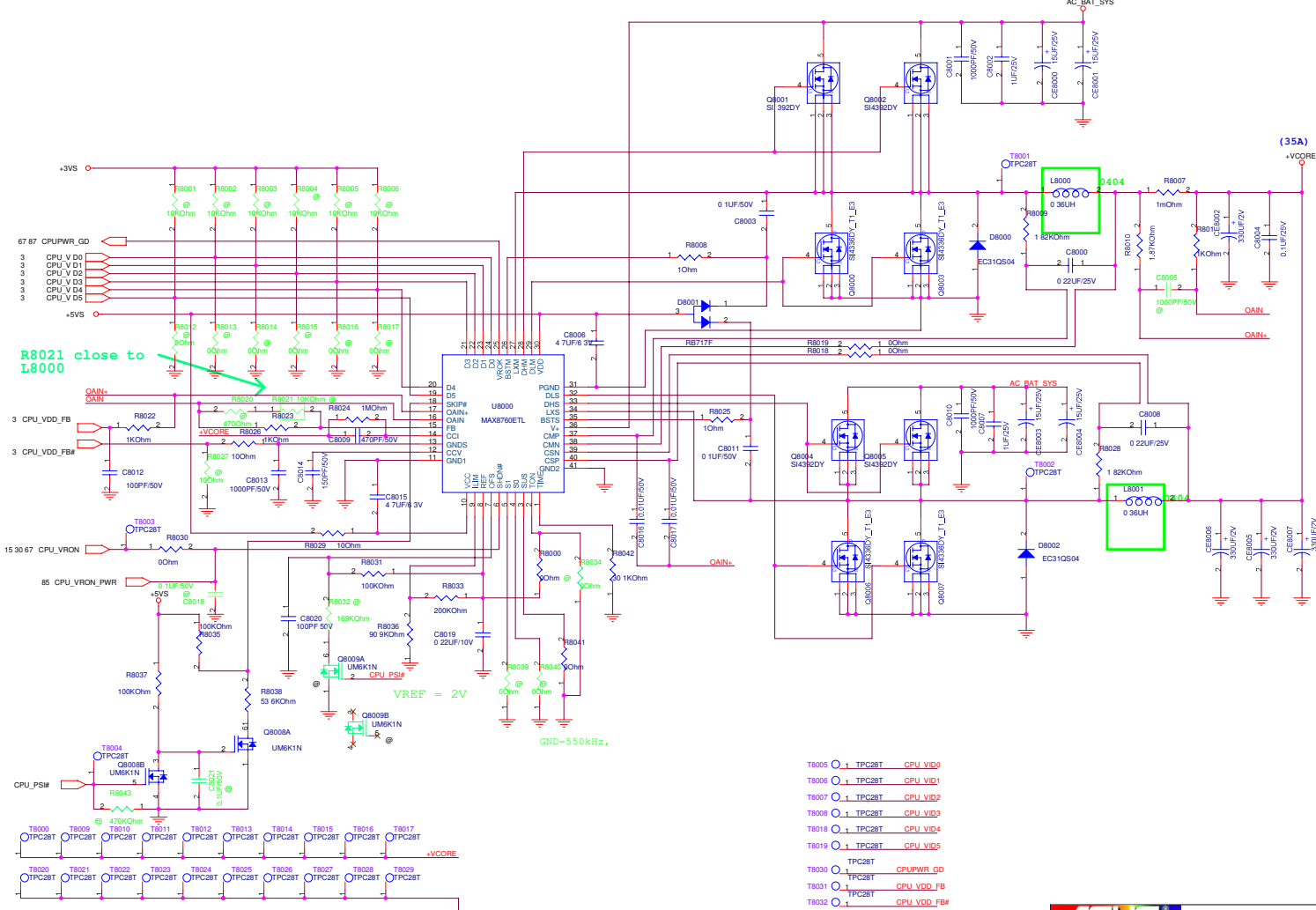
« Kennedy_Zhang »

		Title : FSU	
ASUSTeK Computer INC.		Engineer: He_Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2007-10-26 2007		Sheet: 15 of 25	

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« Kennedy_Zhang »

		Title : FSU	
ASUSTek Computer Inc.		Engineer: He Wang	
Size	Project Name		Rev
C	FSU		1.0
Date: 2012-03-29 10:07		Sheet: 01	of 01

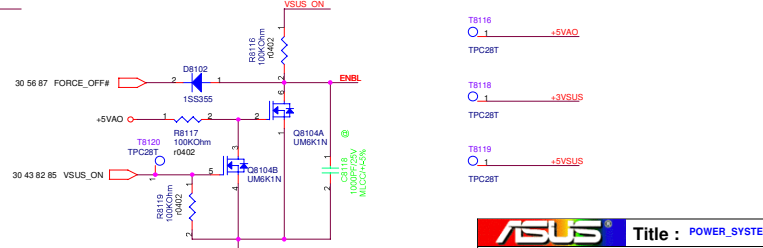
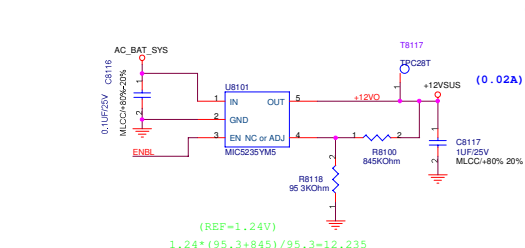
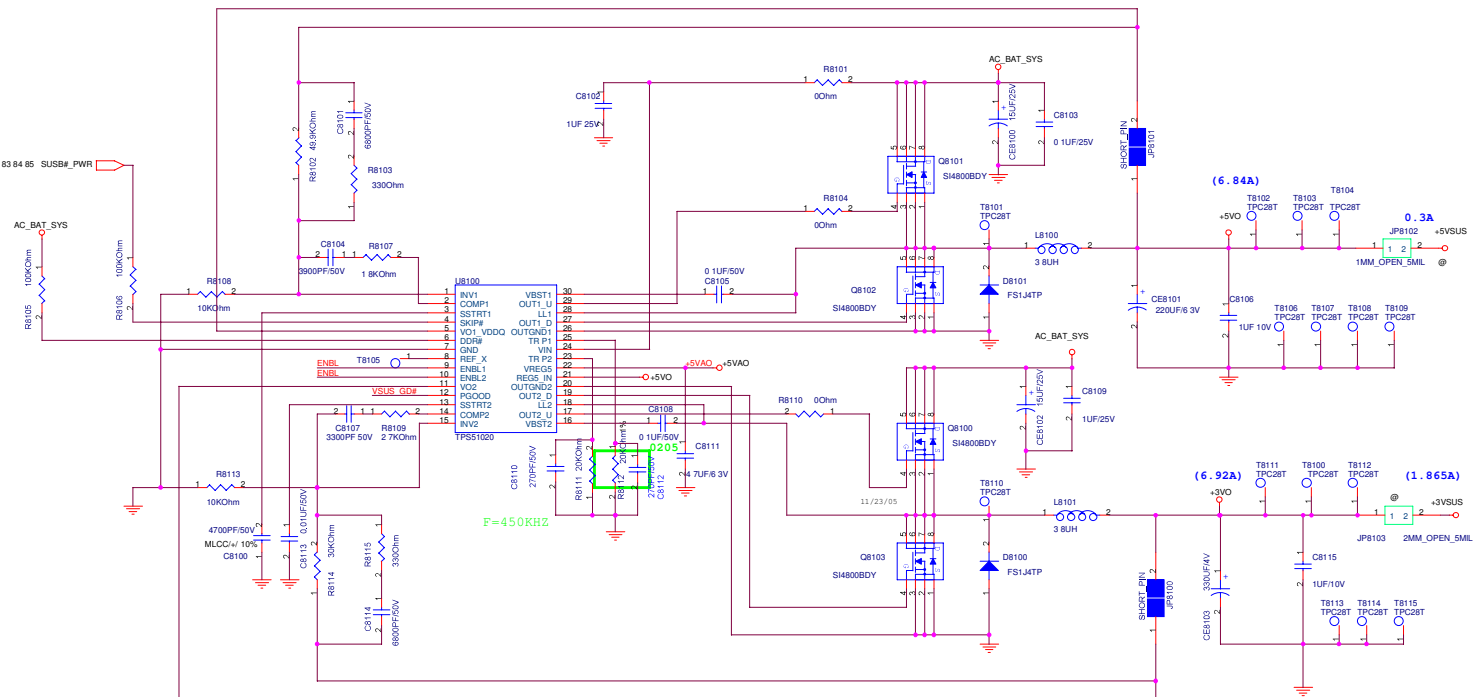


- T8005 1 TPC28T CPU_VD0
- T8006 1 TPC28T CPU_VD1
- T8007 1 TPC28T CPU_VD2
- T8008 1 TPC28T CPU_VD3
- T8018 1 TPC28T CPU_VD4
- T8019 1 TPC28T CPU_VD5
- T8030 1 TPC28T CPU_PWR_GD
- T8031 1 TPC28T CPU_VDD_FB
- T8032 1 TPC28T CPU_VDD_FB#

ASUS		Title : POWER_VCORE	
ASUSTek	Project Name	Engineer: li-mei_chen	
Size	Custom	F5N	Rev 1.1
Date: 11/11/08 2007	Sheet 80	of 88	

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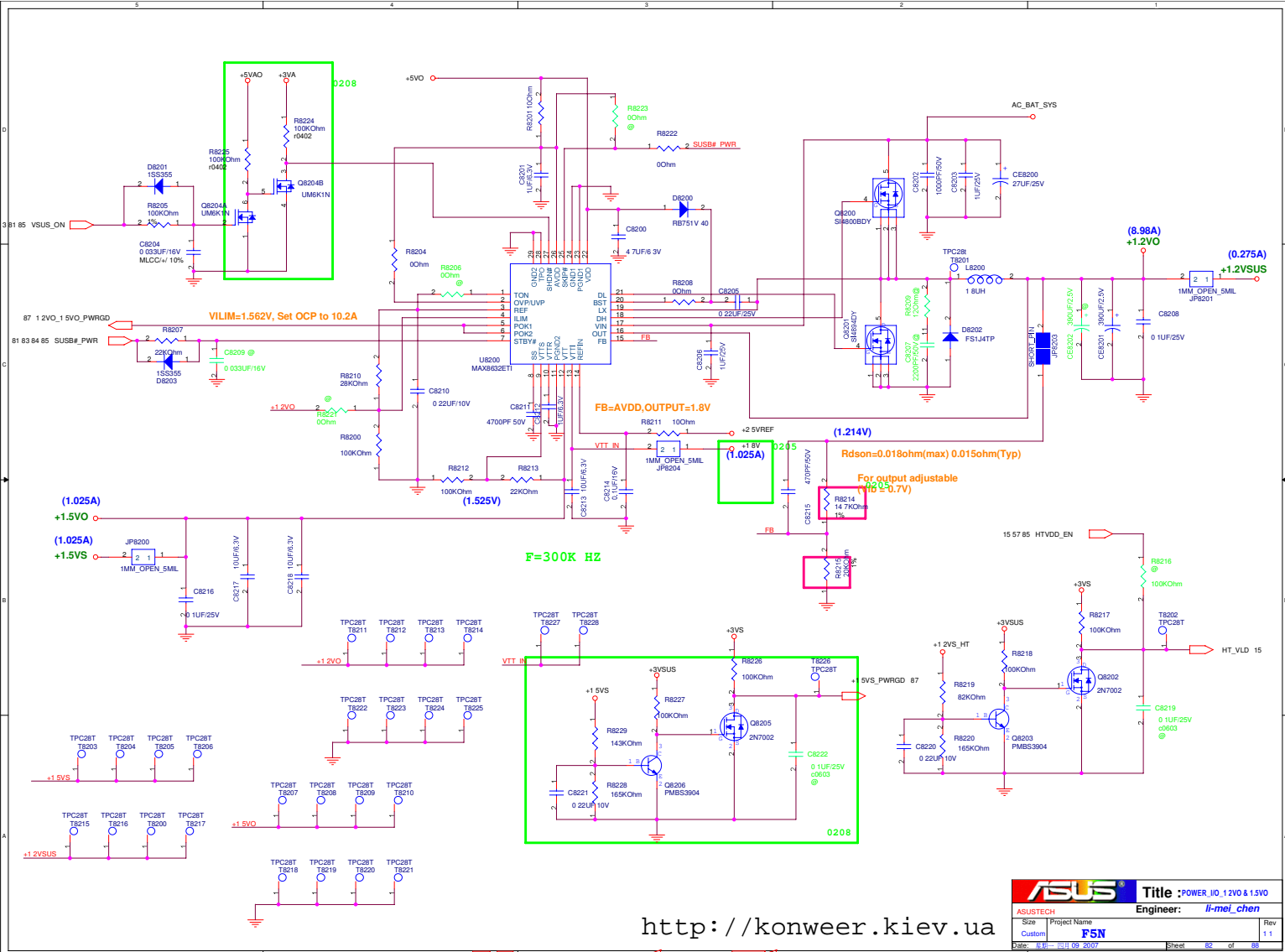
<< Kennedy_Zhang >>



ASUS		Title : POWER_SYSTEM	
ASUSTECH	Project Name	Engineer:	ji-mei_chen
Size	Custom	F5N	Rev 1.1
Date:	09/09/2007	Sheet	81 of 88

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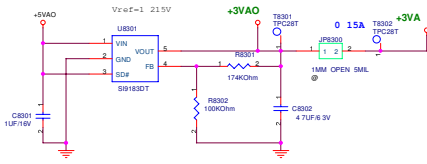
<< Kennedy_Zhang >>



ASUS		Title :POWER_I/O_1.2V & 1.5V	
ASUSTECH	Project Name	Engineer:	li-mei_chen
Size	Custom	F5N	Rev 1.1
Date: 11/09/2007	Sheet 82	of 88	

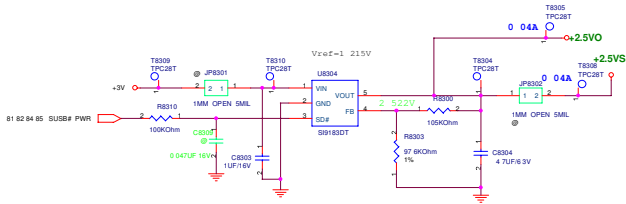
<http://konweer.kiev.ua>

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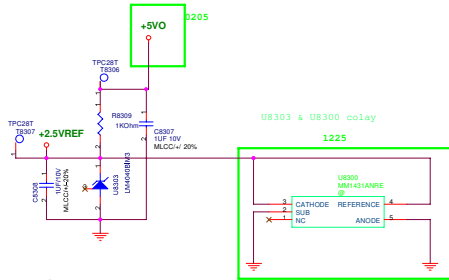


+3VA

+2.5VS



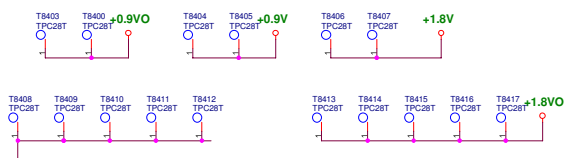
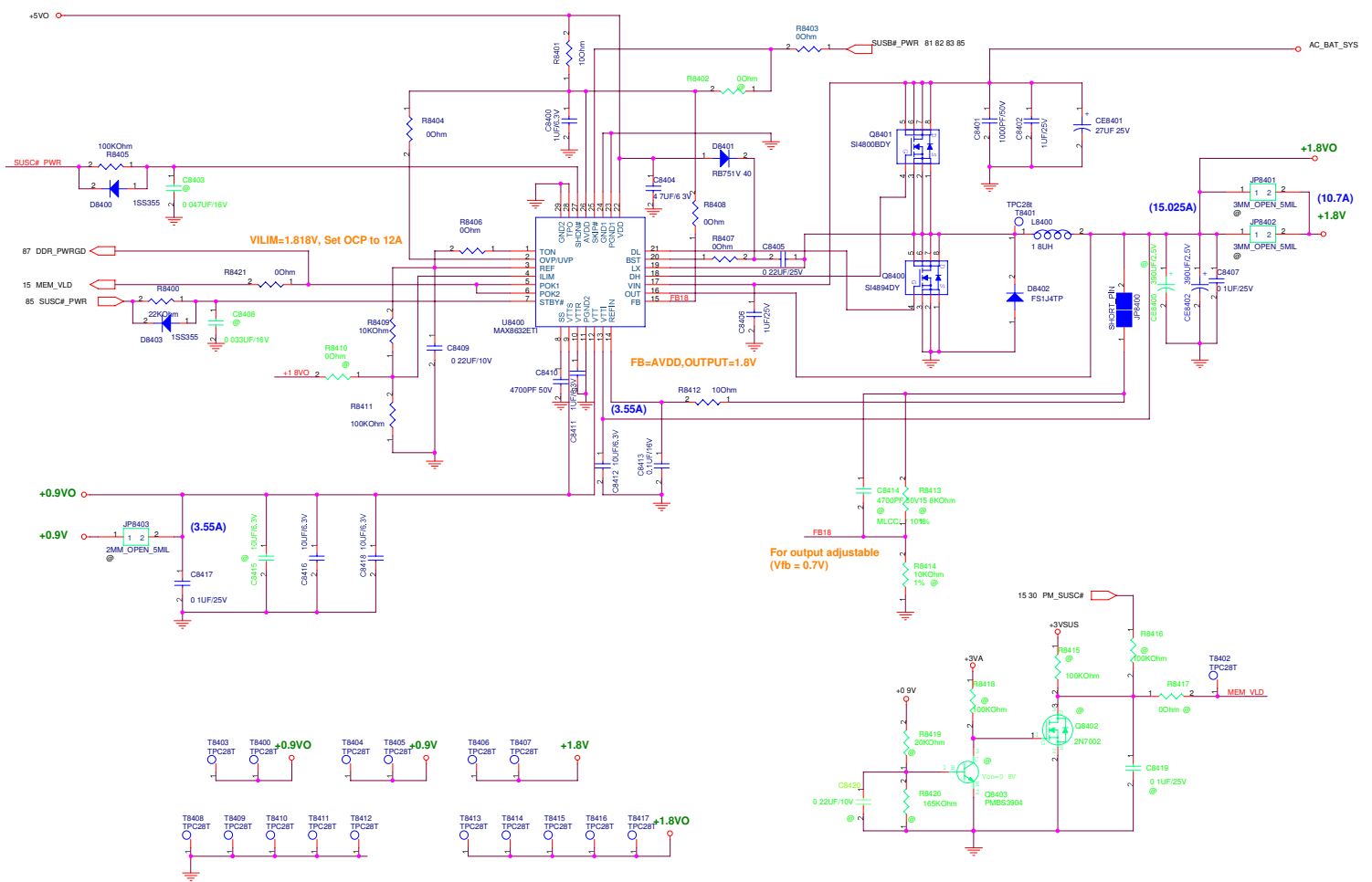
+2.5VREF



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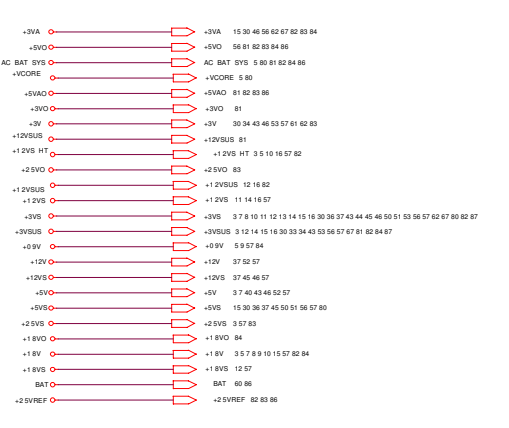
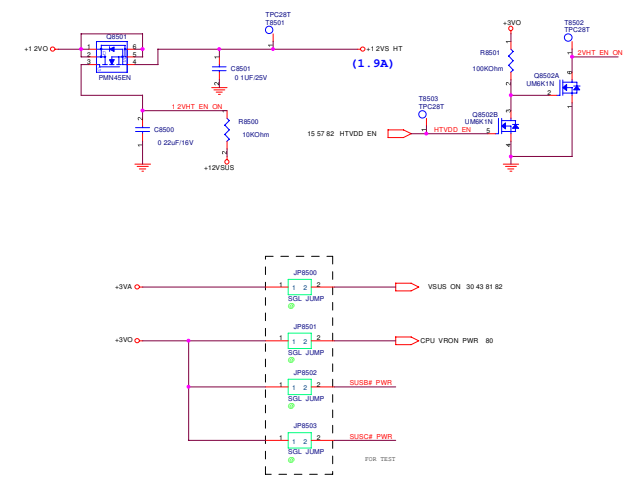
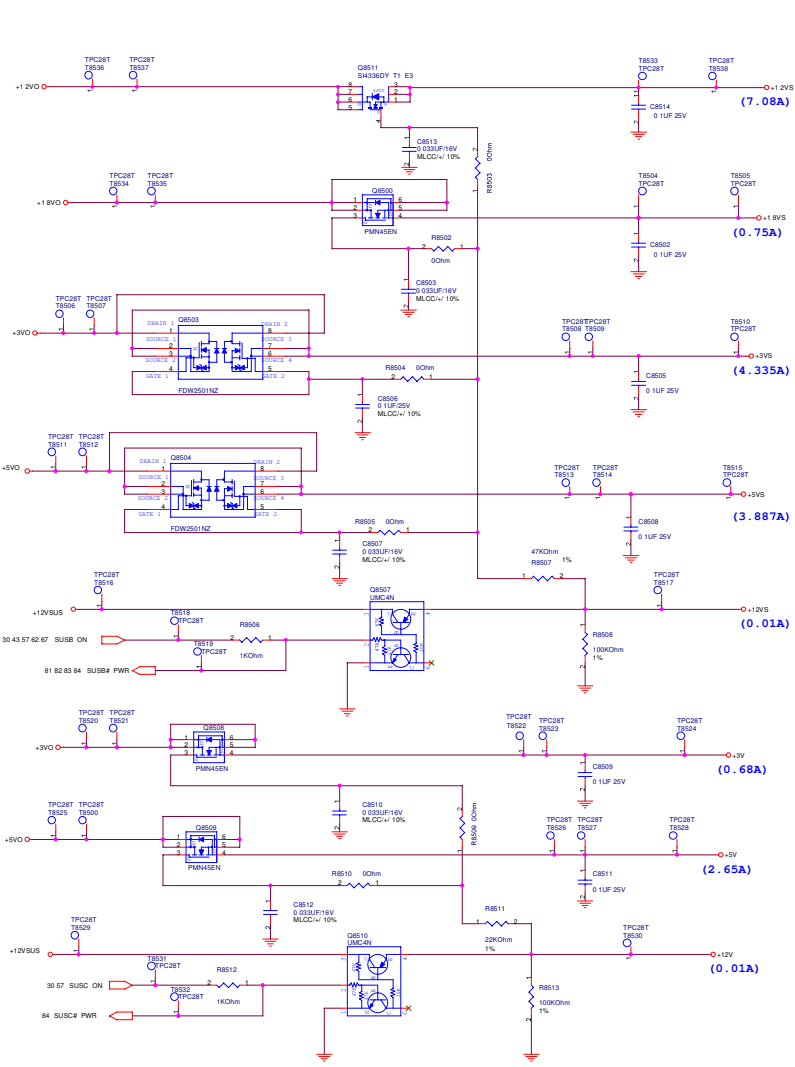
ASUS		Title : POWER_IO_LDO	
ASUSTECH	Project Name	Engineer: li mei_chen	Rev
1	1	FSN	1.1
Date: 2011-05-19 20:00	Sheet: 61	of	68



ASUS		Title : POWER_IQ_D0R1	
ASUSTECH	Project Name	Engineer:	li-mei_chen
Custom	F5N		
Date: 11/11/09 2007	Sheet	84	of 88

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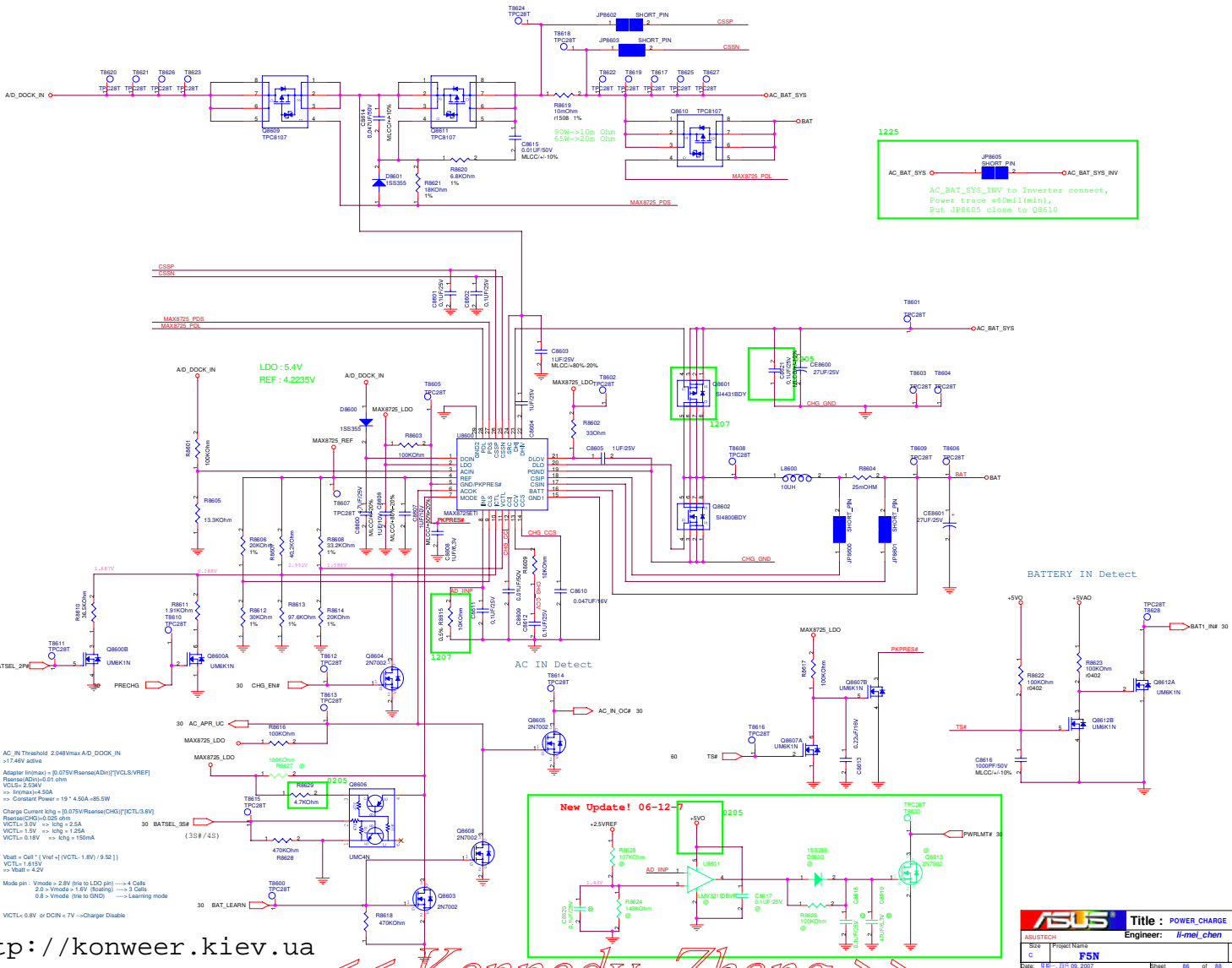


+3VA	15 30 46 56 62 67 82 83 84
+5VD	56 81 82 83 84 86
AC BAT SYS	5 80 81 82 84 86
+VCORE	5 80
+5VAD	81 82 83 86
+2V0	81
+2V	30 34 43 46 53 57 61 62 83
+12VUS 01	12VUS 01
+1.2V0 HT	1.2V0 HT 3 5 10 15 57 82
+2.5V0	83
+1.2VUS0	1.2VUS0 12 16 82
+1.2VUS	1.2VUS 11 14 16 57
+3V0	3 7 8 10 11 12 13 14 15 16 30 36 37 43 44 45 46 50 51 53 56 57 62 67 80 82 87
+3VUS	12 14 15 16 30 33 34 43 53 56 57 67 81 82 84 87
+0.9V	5 9 57 84
+1.2V	37 52 57
+12V0	37 45 46 57
+5V	3 7 40 43 46 52 57
+5VUS	15 30 36 37 45 50 51 56 57 80
+2.5V0	3 57 83
+1.8V0	84
+1.8V	3 5 7 8 9 10 15 57 82 84
+1.8V0	12 57
BAT	60 86
+2.5VREF	82 83 86

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ASUS		Title : POWER_LOAD_SYSTEM	
ASUSTECH		Engineer: li mei_chen	
Rev	0	Project Name	
Rev	1	File Name	F5N
Date	08-19-2007	Sheet	65 of 86



1225
 AC_BAT_SYS_INV → JP8605 SHORT_PIN → AC_BAT_SYS_INV
 AC_BAT_SYS_INV to Inverter connect,
 Power trace = 60mil(min),
 Put JP8605 close to Q8610

LDO: 5.4V
 REF: 4.2235V

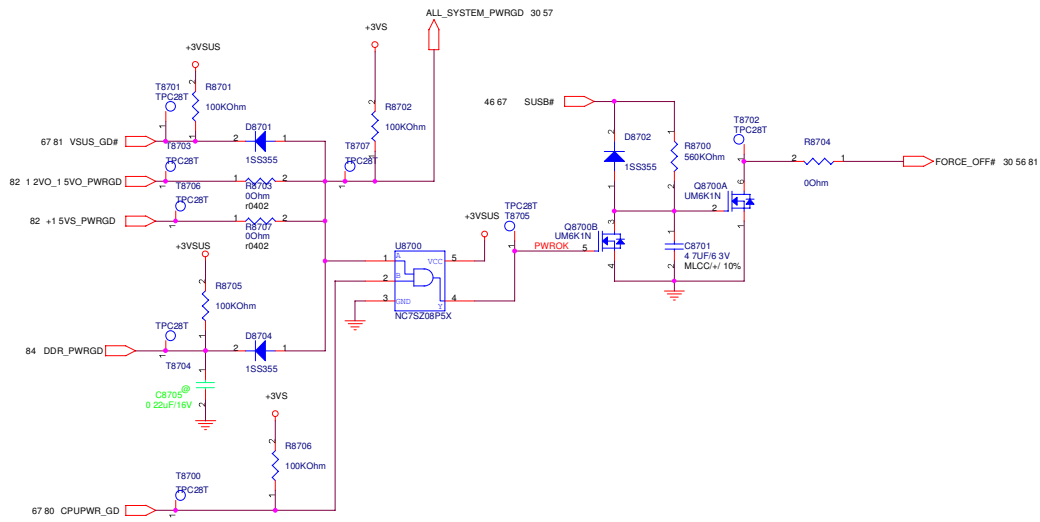
New Update! 06-12-17
 U800
 +2.5VREF
 R825 1070Kohm
 R824 430Kohm
 R826 155355
 C802 0.1uF/25V
 R827 100Kohm
 C803 10uF/25V
 R828 100Kohm
 C804 10uF/25V
 R829 100Kohm
 C805 10uF/25V
 R830 100Kohm
 C806 10uF/25V
 R831 100Kohm
 C807 10uF/25V
 R832 100Kohm
 C808 10uF/25V
 R833 100Kohm
 C809 10uF/25V
 R834 100Kohm
 C810 10uF/25V
 R835 100Kohm
 C811 10uF/25V
 R836 100Kohm
 C812 10uF/25V
 R837 100Kohm
 C813 10uF/25V
 R838 100Kohm
 C814 10uF/25V
 R839 100Kohm
 C815 10uF/25V
 R840 100Kohm
 C816 10uF/25V
 R841 100Kohm
 C817 10uF/25V
 R842 100Kohm
 C818 10uF/25V
 R843 100Kohm
 C819 10uF/25V
 R844 100Kohm
 C820 10uF/25V
 R845 100Kohm
 C821 10uF/25V
 R846 100Kohm
 C822 10uF/25V
 R847 100Kohm
 C823 10uF/25V
 R848 100Kohm
 C824 10uF/25V
 R849 100Kohm
 C825 10uF/25V

- AC_IN Threshold 2.048Vmax A/D_DOCK_IN
 => 1749V active
- Adapter In(max) = (0.075V/Rsense/A/D)/[VCL5/VREF]
 Rsense=400mΩ/0.01 ohm
 VCL5= 1.55V
 => In(max)=4.50A
 => Constant Power = 19 ~ 4.50A * 85.5W
- Charge Current Ichg = (0.075V/Rsense/CHG)/[VCL3/VREF]
 Rsense=400mΩ/0.02 ohm
 VCL3= 3.0V => Ichg = 2.5A
 VCL4= 1.5V => Ichg = 1.25A
 VCL4= 0.18V => Ichg = 150mA
- Vbat = Cell * [Vref - (VCL1 - 1.8V) / 9.52]
 VCL1 = 1.815V
 => Vbat = 4.5V
- Mode pin : Vmode > 2.8V (High to LDO pin) => 4 Cells
 2.8 > Vmode > 1.6V (Floating) => 3 Cells
 0.8 > Vmode (pin to GND) => Learning mode
- VCL1= 0.6V or DCIN = 7V =>Charger Disable

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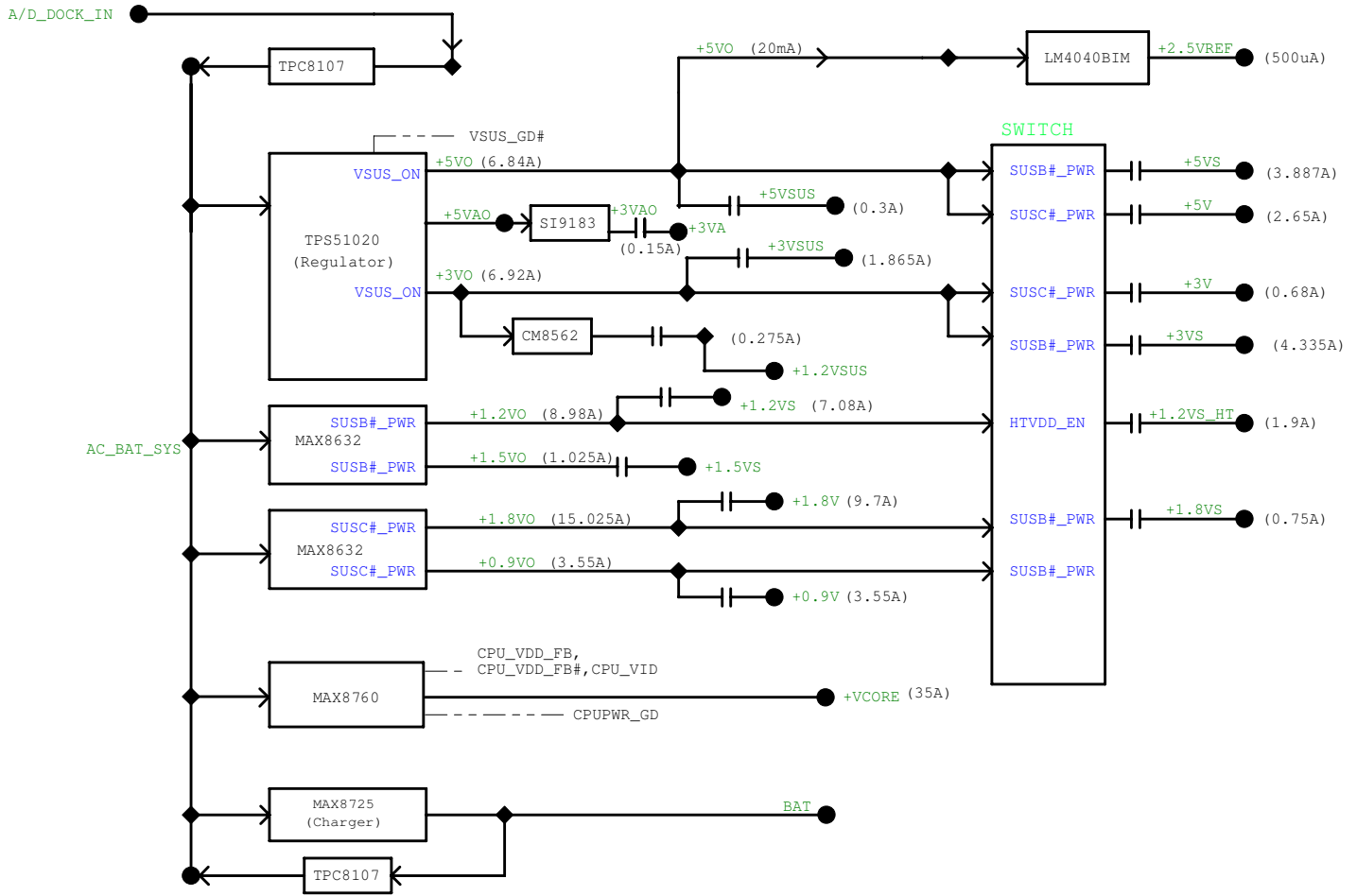
ASUS		Title : POWER_CHARGE	
ASUSTECH	Project Name	Engineer: li-mei_chen	Rev
C	F5N		1.1
Date: 2017-06-19-2017	Sheet: 65	of	66



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ASUS		Title : POWER_PROTECT
ASUSTECH	Project Name	Engineer: <i>li-mei_chen</i>
S_ZS	Custom	F5N
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ASUS		Title : POWER DIAGRAM	
ASUSTECH	Project Name	Engineer: li-mei_chen	
Size	Custom	F5N	Rev 1.1
Date: 10/10/09 2007	Sheet 88	of 88	

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